Evaluation Board for the AD3530/AD3530R 16-Bit, 8-Channel, Voltage Output DACs

FEATURES

- Full featured evaluation board for the AD3530/AD3530R
- Various link options
- PC control when used in conjunction with the Analog Devices, Inc., SDP board

EVALUATION KIT CONTENTS

- EVAL-AD3530RARDZ

HARDWARE REQUIRED

- EVAL-SDP-CK1Z (SDP-K1) board; must be purchased separately

SOFTWARE REQUIRED

- ACE software with the EVAL-AD3530RARDZ plug-in
- SDP-K1 firmware for EVAL-AD3530RARDZ (included in the plug-in)

GENERAL DESCRIPTION

This user guide details the operation of the EVAL-AD3530RARDZ for the AD3530/AD3530R 16-bit, 8-channel, voltage output, digital-to-analog converters (DACs).

The EVAL-AD3530RARDZ allows users to quickly prototype the AD3530/AD3530R circuits and reduce design time. The AD3530/AD3530R operate from single 2.7V to 5.5V supply ranges. The AD3530/AD3530R incorporate internal 2.5V references to give output voltages of 2.5V or 5V. The EVAL-AD3530RARDZ also incorporates an additional on-board voltage reference, the ADR4525.

The EVAL-AD3530RARDZ interfaces to the USB port of a PC via a system demonstration platform (SDP-K1) board. The Analysis | Control | Evaluation (ACE) software is available for download from the EVAL-AD3530RARDZ product page to use with the evaluation board to allow the user to program the AD3530/AD3530R devices. A peripheral module interface (PMOD) connection is also available to allow the connection of microcontrollers to the evaluation board without the SDP-K1 board. When a microcontroller is used through the PMOD connection, the SDP-K1 board must be disconnected, and the user is unable to operate the ACE software.

The EVAL-AD3530RARDZ is compatible with Analog Devices, Inc., SDP-K1 board, which can be purchased separately. A typical connection between the EVAL-AD3530RARDZ and the SDP-K1 controller board is shown in EVAL-AD3530RARDZ.

For full details, see the AD3530/AD3530R data sheet, which must be used in conjunction with this user guide when using the EVAL-AD3530RARDZ.

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REVISION HISTORY

1/2025—Revision 0: Initial Version

EVALUATION BOARD SOFTWARE QUICK START PROCEDURES

INSTALLING ACE

The EVAL-AD3530RARDZ uses the ACE evaluation software that allows the evaluation and control of multiple evaluation systems.

The ACE installer installs the necessary SDP-K1 drivers and the Microsoft® .NET Framework 4 by default. The ACE software is available for download from the ACE Software page and must be installed before connecting the SDP-K1 board to the USB port of the PC to ensure that the SDP-K1 board is recognized as connected to the PC. For full instructions on how to install and use this software, see the ACE Software page on the Analog Devices website.

INITIAL SETUP

To set up the evaluation board, take the following steps:

1. Connect the evaluation board to the SDP-K1 board, and then connect the USB cable between the SDP-K1 board and the PC.
2. Run the ACE application. The EVAL-AD3530RARDZ plug-ins appear in the attached hardware section of the **Start** tab.
3. Double-click the board plug-in to open the board view seen in Figure 1. When the correct firmware in the SDP-K1 controller is missing or does not match, a dialogue box shown in Figure 2 will pop-up and will give the option to flash the correct firmware. Press **OK** for initial setup. Press **Cancel** if the firmware has been loaded previously.
4. Double-click the AD3530R chip to access the chip block diagram. This view provides a basic representation of the functionality of the board. The main functions of the board are labeled in Figure 3 .

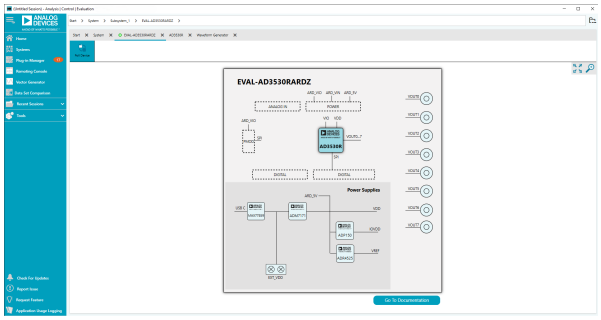

Figure 1. Board View of the EVAL-AD3530RARDZ

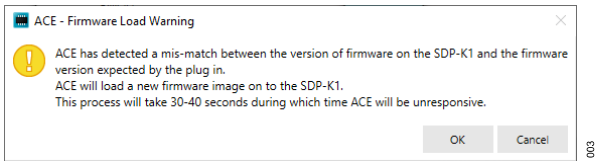

Figure 2. Firmware Loading Dialogue Box

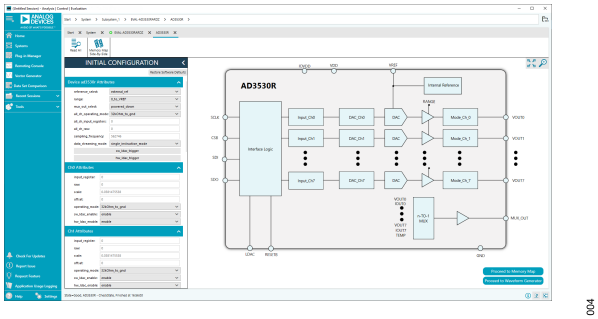

Figure 3. Chip Block Diagram of the AD3530R

EVALUATION BOARD HARDWARE

POWER SUPPLIES

The EVAL-AD3530RARDZ provides on-board 1.8V, 3.3V, and 5V regulators powered through the SDP-K1 or from an external source (USB-C connector or terminal block). If a different supply is required or if the evaluation board is controlled through the PMOD connector, an external supply must be provided by the USB-C connector or through the terminal block. See Table 1 for more details.

LINK OPTIONS

A number of link options are incorporated on the EVAL-AD3530RARDZ and must be set for the required operating conditions before using the board. The functions of these link options are described in Table 2.

Table 1. Power Supply Connectors

Connector Label	Voltage Supplies Description
M1 (USB-C Connector)	5V supply. Requires a USB charger adapter that complies with the USB charging specification 1.2. Use this if the total load of the part is more than 100mA.
P6 (TERM_BLOCK)	5.2V to 6.5V input. Use this if the total load is more than 100mA and if the USB charger adapter is not preferred. JP1 should be set to Position B.
P2, Pin 5 (ARD_5V)	If the overall load required is less than 100mA, ARD_5V can be used as a source and, with SDP-K1, powered through the USB. JP3 should be set to Position B.

Table 2. Link Functions

Link	Default Position	Description
JP1	A	This link selects the external supply for the on board ADM7171 LDO. Position A: External supply used will be through the USB-C connector. An USB-C based charger adapter must be connected. The ADM7171 is powered by a 6V rail coming from the MAX77859. Position B: External supply used will be through the 2-pin terminal block, P6. An acceptable voltage range for this input is 5.2V to 6.5V.
JP2	Short	This link selects the voltage setpoint for the LDO stage (ADM7171). Short: Voltage setpoint is 5V. Open: Voltage setpoint is 3.3V.
JP3	B	This link selects the VDD source. There are two options, as follows: Position A: Takes either 5V or 3.3V generated by LDO stage. Please refer to JP2 orientation. Position B: Takes 5V generated by the SDP-K1 or any controller connected to the Arduino connector. Loads sourced by AD3530/AD3530R should not exceed 100mA.
JP4	Open	This link selects the availability of on-board reference voltage (the ADR4525) to the VREF pin of the device. The options are as follows: Short: VREF supplies reference voltage to the AD3530/AD3530R. The reference control register must be set such that the on-chip reference is disabled. Open: Required if on-chip reference is enabled or reference voltage is supplied through J2.
JP5	Short	This link enables/disables the MAX13030 level shifter. Shorted: Enables the MAX13030. Digital communication can come from either the SDP-K1 controller board or the PMOD connector, P5. Opened: Disables the MAX13030.

EVALUATION BOARD HARDWARE

OTHER ON-BOARD CONNECTORS

Table 3 shows the connectors on the EVAL-AD3530RARDZ.

Table 3. Other On-Board Connectors

Connector	Function
J2 (unmounted)	Male Subminiature Version B (SMB) port for external reference. Prior to use, P11 must be opened, and the on-chip reference of the AD3530/AD3530R must be disabled through the reference control register.
P5 (unmounted)	12-pin SPI PMOD Connector. Use this if a separate digital interface, aside from Arduino form factor controller (the SDP-K1, for example), will be used.
J1 (unmounted)	Male SMB port to measure the DAC output voltage.
P8 (unmounted)	Short a pair of rows to temporarily select the channel to be measured on J1. Mounting one 0Ω on either of the resistors from R78 to R85 can also be used. Only one method and one channel can be selected at a time.
P7	Male headers to monitor or connect external loads to each DAC output channel.

EVALUATION BOARD SCHEMATICS AND ARTWORK

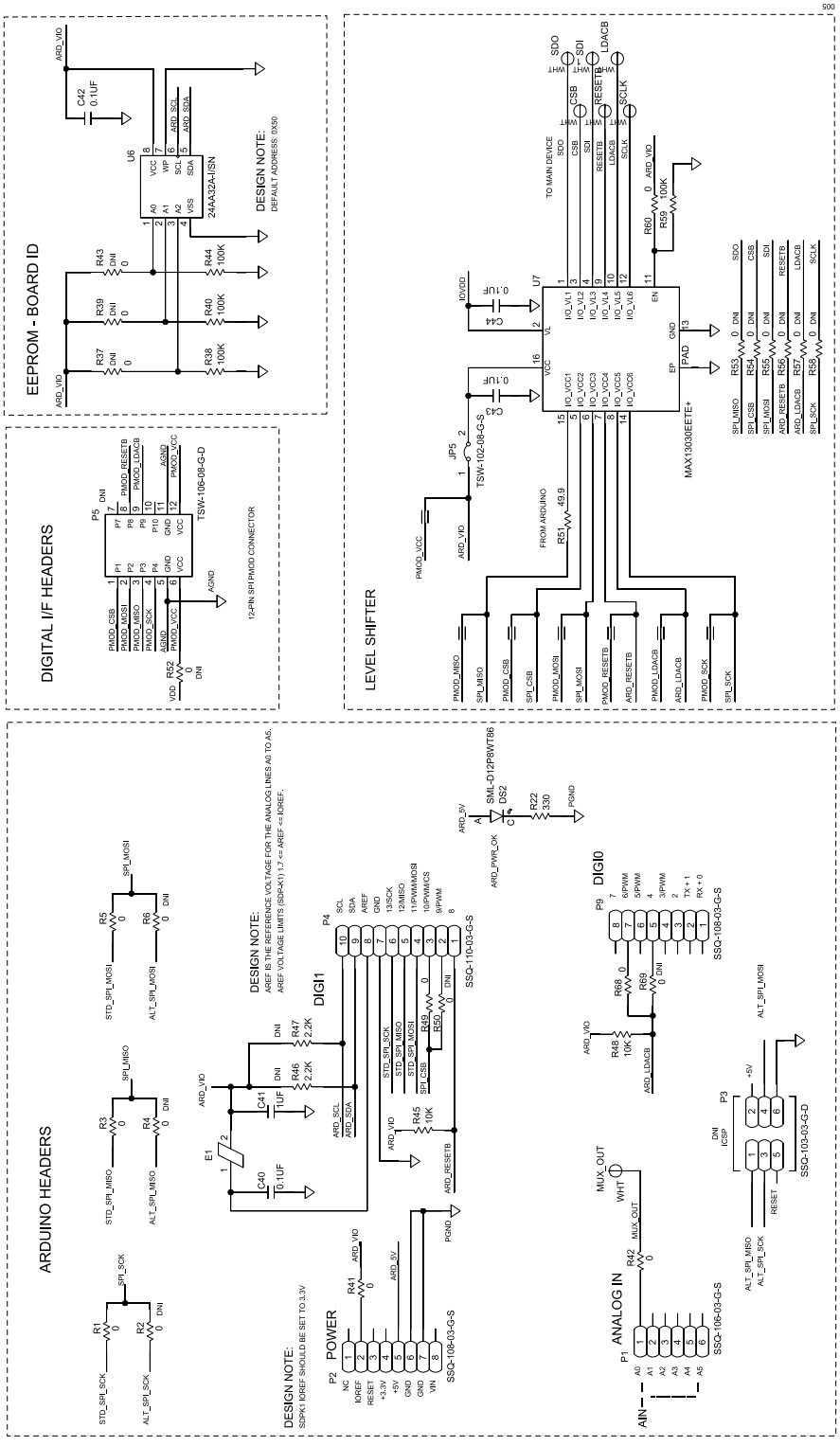

Figure 4. EVAL-AD3530RARDZ Schematic, SDP, Arduino®-Compatible, and PMOD Connectors

EVALUATION BOARD SCHEMATICS AND ARTWORK

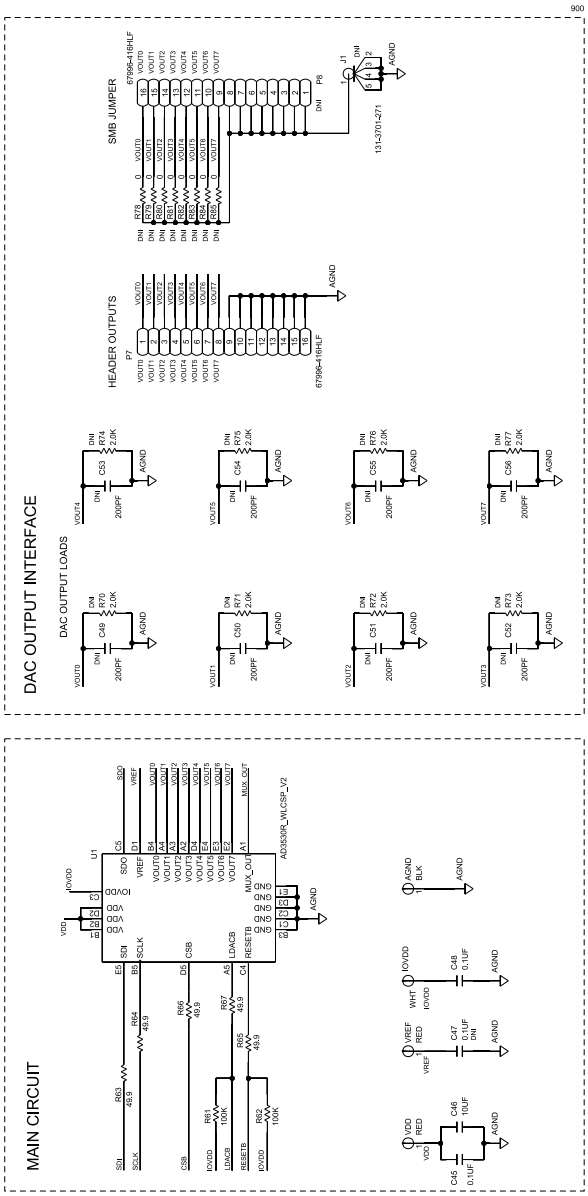

Figure 5. EVAL-AD3530RARDZ Schematic, Main Circuit and Output Connectors

EVALUATION BOARD SCHEMATICS AND ARTWORK

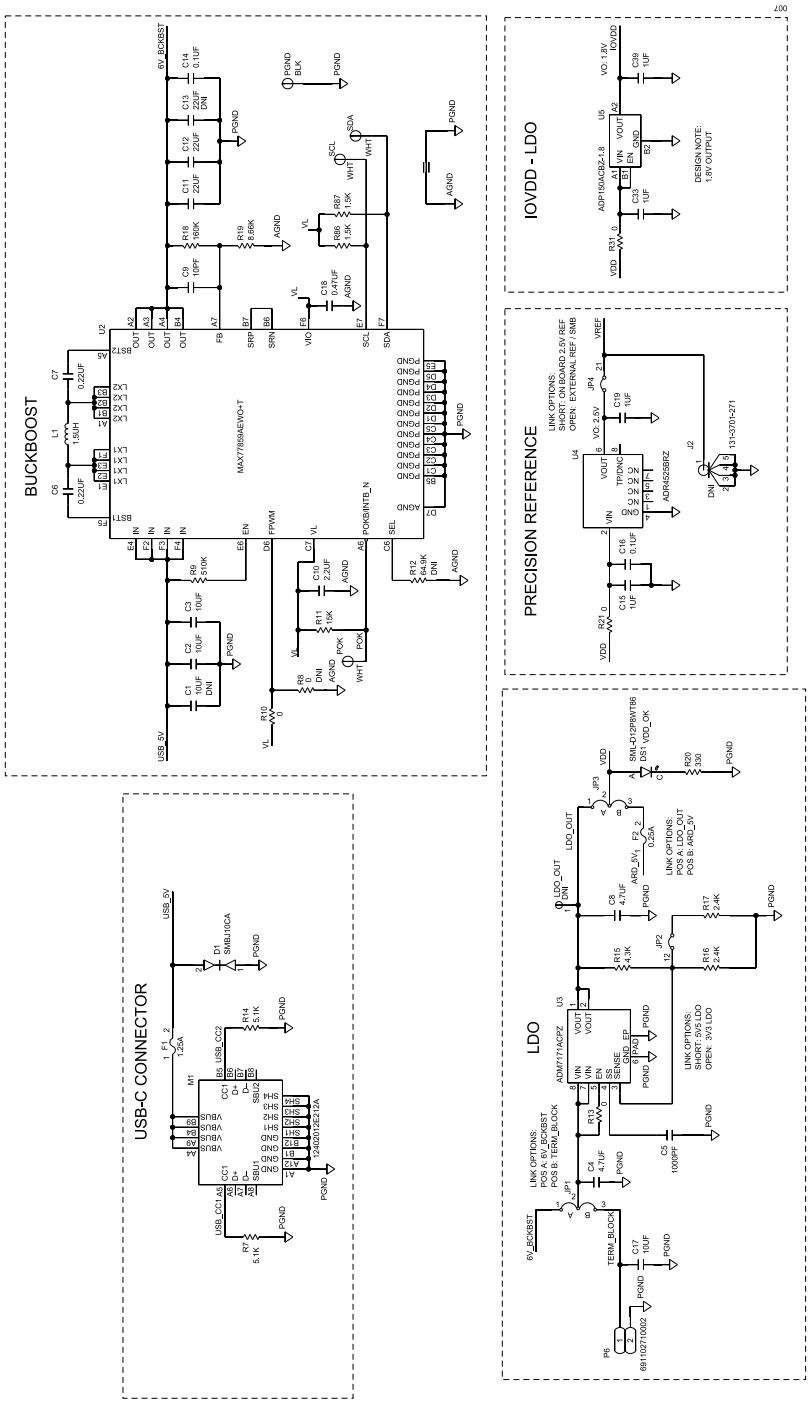

Figure 6. EVAL-AD3530RARDZ Schematic, Power Tree and On-Board Reference

EVALUATION BOARD SCHEMATICS AND ARTWORK

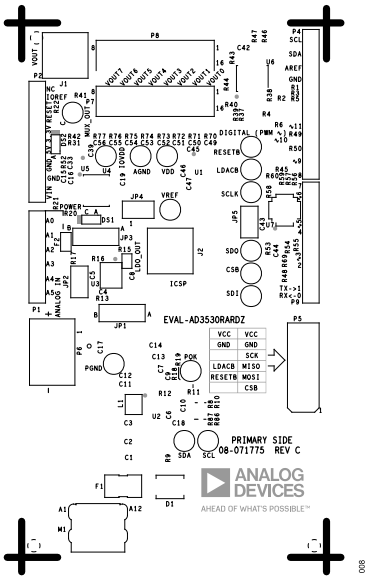

Figure 7. EVAL-AD3530RARDZTop Side Component Placement

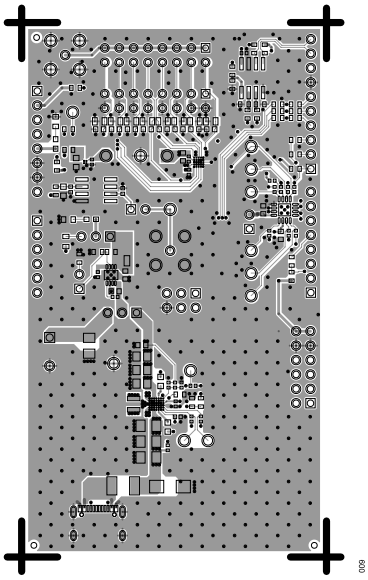

Figure 8. EVAL-AD3530RARDZ Top Side Routing

EVALUATION BOARD SCHEMATICS AND ARTWORK

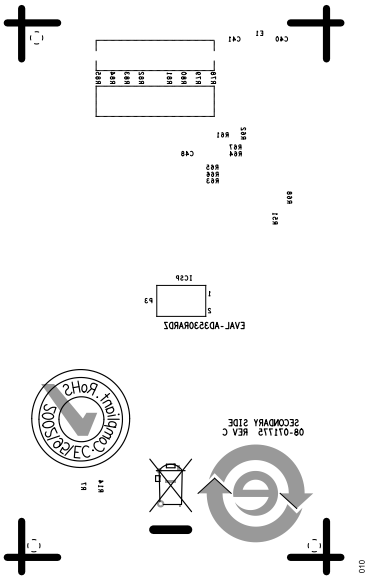

Figure 9. EVAL-AD3530RARDZ Bottom Side Component Placement

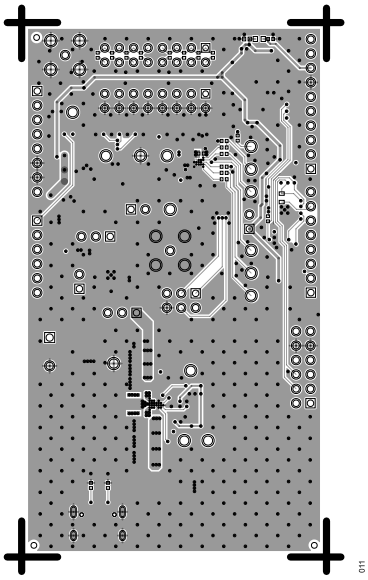

Figure 10. EVAL-AD3530RARDZ Bottom Side Routing

ORDERING INFORMATION

ESD Caution
ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.