



RELIABILITY REPORT FOR MAX9065EUK+

PLASTIC ENCAPSULATED DEVICES

February 6, 2009

# MAXIM INTEGRATED PRODUCTS

120 SAN GABRIEL DR. SUNNYVALE, CA 94086

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#### Conclusion

The MAX9065EUK+ successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim's quality and reliability standards.

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#### I. Device Description

A. General

The MAX9065 is an ultra-small, low-power, window comparator ideal for a wide variety of portable electronics applications such as cell phones, portable media players, and notebooks that have extremely tight board space and power constraints. It comes in both a 4-bump UCSP package with a 1mm x 1mm footprint (as small as two 0402 resistors) and a 5-pin SOT23 package. The MAX9065 features a common-mode input range of -0.3V to +5.5V independent of supply voltage. The input current goes to zero when the MAX9065 is powered down (VCC = 0). Additionally, the MAX9065 features high RF immunity. The MAX9065 has a push-pull output and consumes only 1 $\mu$ A (max) supply current. The MAX9065 operates down to 1.0V over the extended -40°C to +85°C temperature range.



II. Manufacturing Information

Ultra-Small, Low-Power Window Comparator in 4 UCSP(tm) and 5-SOT23

Carsem Malalysia, UTL Thailand, ISPL Philippines

- A. Description/Function:
- B. Process:
- C. Number of Device Transistors:
- D. Fabrication Location:
- E. Assembly Location:
- F. Date of Initial Production:

## III. Packaging Information

A. Package Type:	5-pin SOT23
B. Lead Frame:	Copper
C. Lead Finish:	100% matte Tin
D. Die Attach:	Conductive Epoxy
E. Bondwire:	Au (1.0 mil dia.)
F. Mold Material:	Epoxy with silica filler
G. Assembly Diagram:	#
H. Flammability Rating:	Class UL94-V0
I. Classification of Moisture Sensitivity per JEDEC standard J-STD-020-C	Level 1
J. Single Layer Theta Ja:	324.3°C/W
K. Single Layer Theta Jc:	82°C/W

#### IV. Die Information

Dimensions:	41 X 41 mils
Passivation:	$Si_3N_4/SiO_2$ (Silicon nitride/ Silicon dioxide
Interconnect:	Aluminum/Si (Si = 1%)
Backside Metallization:	None
Minimum Metal Width:	0.8 microns (as drawn)
Minimum Metal Spacing:	0.8 microns (as drawn)
Bondpad Dimensions:	5 mil. Sq.
Isolation Dielectric:	SiO <sub>2</sub>
Die Separation Method:	Wafer Saw
	Passivation: Interconnect: Backside Metallization: Minimum Metal Width: Minimum Metal Spacing: Bondpad Dimensions: Isolation Dielectric:

B8 202

Texas

July 26, 2008



#### V. Quality Assurance Information

A. Quality Assurance Contacts:	Ken Wendel (Director, Reliability Engineering) Bryan Preeshl (Managing Director of QA)
B. Outgoing Inspection Level:	0.1% for all electrical parameters guaranteed by the Datasheet. 0.1% For all Visual Defects.
C. Observed Outgoing Defect Rate:	< 50 ppm
D. Sampling Plan:	Mil-Std-105D

#### VI. Reliability Evaluation

#### A. Accelerated Life Test

The results of the 135°C biased (static) life test are shown in Table 1. Using these results, the Failure Rate ( $\lambda$ ) is calculated as follows:

$$\begin{split} \lambda &= \underbrace{1}_{\text{MTTF}} &= \underbrace{1.83}_{192 \text{ x} 4340 \text{ x} 48 \text{ x} 2} (\text{Chi square value for MTTF upper limit}) \\ & (\text{where } 4340 = \text{Temperature Acceleration factor assuming an activation energy of 0.8eV}) \\ & \lambda &= 22.4 \text{ x } 10^{-9} \\ & \lambda &= 22.4 \text{ F.I.T. (60\% confidence level @ 25°C)} \end{split}$$

The following failure rate represents data collected from Maxim's reliability monitor program. Maxim performs quarterly 1000 hour life test monitors on its processes. This data is published in the Product Reliability Report found at http://www.maxim-ic.com/. Current monitor data for the B8 Process results in a FIT Rate of 2.71 @ 25C and 17.30 @ 55C (0.8 eV, 60% UCL)

#### B. Moisture Resistance Tests

The industry standard 85°C/85%RH or HAST testing is monitored per device process once a quarter.

#### C. E.S.D. and Latch-Up Testing

The CM92-5 die type has been found to have all pins able to withstand a HBM transient pulse of 2500 per JEDEC JESD22-A114-D. Latch-Up testing has shown that this device withstands a current of 250.



# Table 1 Reliability Evaluation Test Results

## MAX9065EUK+

TEST ITEM	TEST CONDITION	FAILURE IDENTIFICATION	SAMPLE SIZE	NUMBER OF FAILURES	
Static Life Test	(Note 1)				
	Ta = 135°C	DC Parameters	48	0	
	Biased	& functionality			
	Time = 192 hrs.				
Moisture Testing	(Note 2)				
85/85	Ta = 85°C	DC Parameters	77	0	
	RH = 85%	& functionality			
	Biased				
	Time = 1000hrs.				
Mechanical Stres	ss (Note 2)				
Temperature	-65°C/150°C	DC Parameters	77	0	
Cycle	1000 Cycles	& functionality			
-	Method 1010				

Note 1: Life Test Data may represent plastic DIP qualification lots.

Note 2: Generic Package/Process data