

RELIABILITY REPORT

FOR

MAX633AESA+

PLASTIC ENCAPSULATED DEVICES

October 21, 2009

# **MAXIM INTEGRATED PRODUCTS**

120 SAN GABRIEL DR. SUNNYVALE, CA 94086

Approved by					
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Quality Assurance					
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## Conclusion

The MAX633AESA+ successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim"s continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim"s quality and reliability standards.

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- I. Device Description

A. General

See data sheet



## II. Manufacturing Information

A. Description/Function: 5V Fixed/Adjustable Output, Step-Up Switching Regulators

B. Process: M6

C. Number of Device Transistors:

D. Fabrication Location: Oregon

E. Assembly Location: Malaysia, Philippines, Thailand

F. Date of Initial Production: Pre 1997

# III. Packaging Information

A. Package Type: 8-pin SOIC (N)
B. Lead Frame: Copper

C. Lead Finish:

D. Die Attach:

Conductive Epoxy

E. Bondwire:

Gold (1.3 mil dia.)

F. Mold Material:

G. Assembly Diagram:

H. Flammability Rating:

100% matte Tin

Conductive Epoxy

Gold (1.3 mil dia.)

Epoxy with silica filler

#05-0701-0083

Class UL94-V0

Classification of Moisture Sensitivity per

JEDEC standard J-STD-020-C

J. Single Layer Theta Ja: 170°C/W
K. Single Layer Theta Jc: 40°C/W
L. Multi Layer Theta Ja: 132°C/W
M. Multi Layer Theta Jc: 38°C/W

### IV. Die Information

A. Dimensions: 70 X 100 mils

B. Passivation: Si<sub>3</sub>N<sub>4</sub>/SiO<sub>2</sub> (Silicon nitride/ Silicon dioxide)

C. Interconnect: Al/1.0%SiD. Backside Metallization: None

E. Minimum Metal Width: Metal1 = 0.5 / Metal2 = 0.6 / Metal3 = 0.6 microns (as drawn)
 F. Minimum Metal Spacing: Metal1 = 0.45 / Metal2 = 0.5 / Metal3 = 0.6 microns (as drawn)

Level 1

G. Bondpad Dimensions: 5 mil. Sq.
 H. Isolation Dielectric: SiO<sub>2</sub>
 I. Die Separation Method: Wafer Saw



### V. Quality Assurance Information

A. Quality Assurance Contacts: Ken Wendel (Director, Reliability Engineering)

Bryan Preeshl (Managing Director of QA)

B. Outgoing Inspection Level: 0.1% for all electrical parameters guaranteed by the Datasheet.

0.1% For all Visual Defects.

C. Observed Outgoing Defect Rate: < 50 ppm</li>D. Sampling Plan: Mil-Std-105D

# VI. Reliability Evaluation

## A. Accelerated Life Test

The results of the 135°C biased (static) life test are shown in Table 1. Using these results, the Failure Rate ( 3) is calculated as follows:

$$\lambda = \underbrace{\frac{1}{\text{MTTF}}}_{\text{measure}} = \underbrace{\frac{10.47}{192 \times 4340 \times 240 \times 2}}_{\text{(where 4340 = Temperature Acceleration factor assuming an activation energy of 0.8eV)}}_{\text{measure}}$$

$$\lambda = 25.6 \times 10^{-9}$$

$$\lambda = 25.6 \text{ F.I.T. (60% confidence level @ 25°C)}$$

The following failure rate represents data collected from Maxim"s reliability monitor program. Maxim performs quarterly life test monitors on its processes. This data is published in the Reliability Report found at http://www.maxim-ic.com/qa/reliability/monitor. Cumulative monitor data for the M6 Process results in a FIT Rate of 0.22 @ 25C and 3.73 @ 55C (0.8 eV, 60% UCL)

### B. Moisture Resistance Tests

The industry standard 85°C/85%RH or HAST testing is monitored per device process once a quarter.

# C. E.S.D. and Latch-Up Testing

The PS09-2 die type has been found to have all pins able to withstand a HBM transient pulse of <+/-400 V per Mil-Std 883 Method 3015.7. Latch-Up testing has shown that this device withstands a current of +/-100 mA.



# **Table 1**Reliability Evaluation Test Results

# MAX633AESA+

TEST ITEM	TEST CONDITION	FAILURE IDENTIFICATION	SAMPLE SIZE	NUMBER OF FAILURES	
Static Life Test	(Note 1)				
	Ta = 135°C	DC Parameters	240	4	
	Biased	& functionality			
	Time = 192 hrs.				
Moisture Testing	(Note 2)				
HAST	Ta = 130°C	DC Parameters	77	0	
	RH = 85%	& functionality			
	Biased				
	Time = 96hrs.				
Mechanical Stres	ss (Note 2)				
Temperature	-65°C/150°C	DC Parameters	77	0	
Cycle	1000 Cycles	& functionality			
	Method 1010	•			

Note 1: Life Test Data may represent plastic DIP qualification lots.

Note 2: Generic Package/Process data