RELIABILITY REPORT

FOR

MAX4313ExA

PLASTIC ENCAPSULATED DEVICES

August 16, 2006

MAXIM INTEGRATED PRODUCTS

120 SAN GABRIEL DR. SUNNYVALE, CA 94086

Written by

Jim Pedicord Quality Assurance Manager, Reliability Operations

Conclusion

The MAX4313 successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim's quality and reliability standards.

Table of Contents

I.Device Description
II.Manufacturing Information
III.Packaging Information
IV.Die Information
IV.Die Information

I. Device Description

A. General

The MAX4313 single-supply mux-amp combines high-speed operation, low-glitch switching, and excellent video specifications. The MAX4313 integrate 2-channel multiplexers, respectively, with a +2V/V fixed-gain amplifier. The device has a 40ns channel switching time and low 10mVp-p switching transients, making it ideal for video-switching applications. It operate sfrom a single +4V to +10.5V supply, or from dual supplies of ±2V to ±5.25V, and features Rail-to-Rail outputs and an input common-mode voltage range that extends to the negative supply rail.

The MAX4313, with 150MHz -3dB bandwidths up to a 540V/ μ s slew rate, and a fixed gain of +2V/V, is ideally suited for driving back-terminated cables. Quiescent supply current is as low as 6.1mA, while low-power shutdown mode reduces supply current to as low as 560 μ A and places the outputs in a high-impedance state. The MAX4313's internal amplifiers maintain an open-loop output impedance of only 8 Ω over the full output voltage range, minimizing the gain error and bandwidth changes under loads typical of most rail-to-rail amplifiers. With differential gain and phase errors of 0.06% and 0.08°, respectively, this device is ideal for broadcast video applications.

B. Absolute Maximum Ratings

<u>Item</u>	Rating
Supply Voltage (VCC to VEE) Input Voltage All Other Pins Output Current Short-Circuit Duration (VOUT to GND, VCC or VEE)	12V (VEE - 0.3V) to (VCC + 0.3V) (VEE - 0.3V) to (VCC + 0.3V) ±120mA Continuous
Continuous Power Dissipation (TA = +70°C) 8-Pin SO (derate 5.9mW/°C above +70°C) 8-Pin µMAX (derate 4.1mW/°C above +70°C) Operating Temperature Range Storage Temperature Range Lead Temperature (soldering, 10s)	471mW 330mW -40°C to +85°C -65°C to +150°C +300°C

II. Manufacturing Information

A. Description/Function: High-Speed, Low-Power, Single-Supply 2-Channel, Video Multiplexer-Amplifiers

B. Process: CB20 (Complementary Bipolar Process)

C. Number of Device Transistors: 156

D. Fabrication Location: Oregon, USA

E. Assembly Location: Thailand, Malaysia or Philippines

F. Date of Initial Production: July, 1998

III. Packaging Information

A. Package Type: 8-Lead SO 8-Lead μMAX

B. Lead Frame: Copper Copper

C. Lead Finish: Solder Plate or 100% Matte Tin Solder Plate or 100% Matte Tin

D. Die Attach: Silver-Filled Epoxy Silver-Filled Epoxy

E. Bondwire: Gold (1.0 mil dia.) Gold (1 mil dia.)

F. Mold Material: Epoxy with silica filler Epoxy with silica filler

G. Assembly Diagram: #05-3001-0071 #05-3001-0072

H. Flammability Rating: Class UL94-V0 Class UL94-V0

I. Classification of Moisture Sensitivity

per JEDEC standard J-STD-020-C: Level 1 Level 1

IV. Die Information

A. Dimensions: 58 x 51 mils

B. Passivation: Si₃N₄/SiO₂ (Silicon nitride/ Silicon dioxide)

C. Interconnect: Gold

D. Backside Metallization: None

E. Minimum Metal Width: 2 microns (as drawn)

F. Minimum Metal Spacing: 2 microns (as drawn)

G. Bondpad Dimensions: 5 mil. Sq.

H. Isolation Dielectric: SiO₂

I. Die Separation Method: Wafer Saw

V. Quality Assurance Information

A. Quality Assurance Contacts:

Jim Pedicord (Manager, Reliability Operations) Bryan Preeshl (Managing Director of QA)

B. Outgoing Inspection Level: 0.1% for all electrical parameters guaranteed by the Datasheet. 0.1% For all Visual Defects.

C. Observed Outgoing Defect Rate: < 50 ppm

D. Sampling Plan: Mil-Std-105D

VI. Reliability Evaluation

A. Accelerated Life Test

The results of the 150°C biased (static) life test are shown in **Table 1**. Using these results, the Failure Rate (λ) is calculated as follows:

$$\lambda = \frac{1}{\text{MTTF}} = \frac{1.83}{192 \times 9706 \times 160 \times 2}$$
 (Chi square value for MTTF upper limit)

Thermal acceleration factor assuming a 0.8eV activation energy

 $\lambda = 3.07 \times 10^{-9}$ $\lambda = 3.07 \text{ F.I.T.}$ (60% confidence level @ 25°C)

This low failure rate represents data collected from Maxim's reliability qualification and monitor programs. Maxim also performs weekly Burn-In on samples from production to assure the reliability of its processes. The reliability required for lots which receive a burn-in qualification is 59 F.I.T. at a 60% confidence level, which equates to 3 failures in an 80 piece sample. Maxim performs failure analysis on lots exceeding this level. The following Burn-In Schematic (Spec. # 06-5274) shows the static circuit used for this test. Maxim also performs 1000 hour life test monitors quarterly for each process. This data is published in the Product Reliability Report (**RR-B3A**). Current monitor data for the CB20 Process results in a FIT Rate of 0.17 @ 25C and 2.86 @ 55C (0.8 eV, 60% UCL)

B. Moisture Resistance Tests

Maxim evaluates pressure pot stress from every assembly process during qualification of each new design. Pressure Pot testing must pass a 20% LTPD for acceptance. Additionally, industry standard 85°C/85%RH or HAST tests are performed quarterly per device/package family.

C. E.S.D. and Latch-Up Testing

The OP51 die type has been found to have all pins able to withstand a transient pulse of ± 3000 V, per Mil-Std-883 Method 3015 (reference attached ESD Test Circuit). Latch-Up testing has shown that this device withstands a current of ± 250 mA.

Table 1 Reliability Evaluation Test Results

MAX4313ExA

TEST ITEM	TEST CONDITION	FAILURE IDENTIFICATION	SAMPLE SIZE	NUMBER OF FAILURES
Static Life Tes	t (Note 1)			
	Ta = 150°C Biased Time = 192 hrs.	DC Parameters & functionality	160	0
Moisture Testi	ng (Note 2)			
Pressure Pot	Ta = 121°C P = 15 psi. RH= 100% Time = 168hrs.	DC Parameters & functionality	77	0
85/85	Ta = 85°C RH = 85% Biased Time = 1000hrs.	DC Parameters & functionality	77	0
Mechanical St	ress (Note 2)			
Temperature Cycle	-65°C/150°C 1000 Cycles Method 1010	DC Parameters & functionality	77	0

Note 1: Life Test Data may represent plastic D.I.P. qualification lots. Note 2: Generic Package/Process data

Attachment #1

TABLE II. Pin combination to be tested. 1/2/

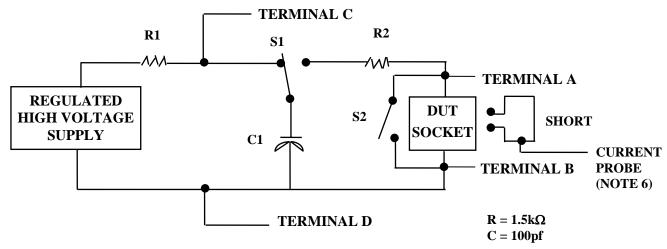
	Terminal A (Each pin individually connected to terminal A with the other floating)	Terminal B (The common combination of all like-named pins connected to terminal B)
1.	All pins except V _{PS1} 3/	All V _{PS1} pins
2.	All input and output pins	All other input-output pins

- 1/ Table II is restated in narrative form in 3.4 below.
- No connects are not to be tested.
 Repeat pin combination I for each named Power supply and for ground

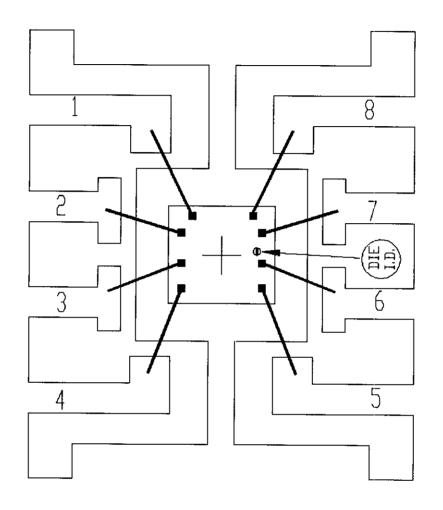
(e.g., where V_{PS1} is V_{DD} , V_{CC} , V_{SS} , V_{BB} , GND, $+V_S$, $-V_S$, V_{RFF} , etc.).

3.4 Pin combinations to be tested.

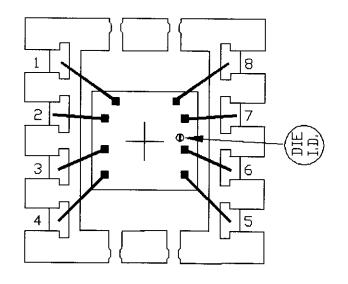
- Each pin individually connected to terminal A with respect to the device ground pin(s) connected a. to terminal B. All pins except the one being tested and the ground pin(s) shall be open.
- Each pin individually connected to terminal A with respect to each different set of a combination b. of all named power supply pins (e.g., V_{SS1} , or V_{SS2} or V_{SS3} or V_{CC1} , or V_{CC2}) connected to terminal B. All pins except the one being tested and the power supply pin or set of pins shall be open.
- Each input and each output individually connected to terminal A with respect to a combination C. of all the other input and output pins connected to terminal B. All pins except the input or output pin being tested and the combination of all the other input and output pins shall be open.



Mil Std 883D Method 3015.7 Notice 8



bkg'code: 28-5		APPROVALS	DATE	NIXIXI	///
CAV./PAD SIZE: 90 X 90	PKG.			BUILDSHEET NUMBER:	REV.:
JO 77 JO	DESIGN			05-3001-0071	<u> </u>



PKG,CODE: U8-1	
CAV./PAD SIZE:	PKG.
68X94	DESIGN

DATE

APPROVALS



