

RELIABILITY REPORT FOR MAX3222ECAP+ PLASTIC ENCAPSULATED DEVICES

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MAXIM INTEGRATED PRODUCTS

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Conclusion

The MAX3222ECAP+ successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim's quality and reliability standards.

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I. Device Description

A. General

The MAX3222E/MAX3232E/MAX3237E/MAX3241E/MAX3246E +3.0V-powered EIA/TIA-232 and V.28/V.24 communications interface devices feature low power consumption, high data-rate capabilities, and enhanced electrostatic-discharge (ESD) protection. The enhanced ESD structure protects all transmitter outputs and receiver inputs to ±15kV using IEC 1000-4-2 Air-Gap Discharge, ±8kV using IEC 1000-4-2 Contact Discharge (±9kV for MAX3246E), and ±15kV using the Human Body Model. The logic and receiver I/O pins of the MAX3237E are protected to the above standards, while the transmitter output pins are protected to ±15kV using the Human Body Model. A proprietary low-dropout transmitter output stage delivers true RS-232 performance from a +3.0V to +5.5V power supply, using an internal dual charge pump. The charge pump requires only four small 0.1µF capacitors for operation from a +3.3V supply. Each device guarantees operation at data rates of 250kbps while maintaining RS-232 output levels. The MAX3237E guarantees operation at 250kbps in the normal operating mode and 1Mbps in the MegaBaud(tm) operating mode, while maintaining RS-232-compliant output levels. The MAX3222E/MAX3232E have two receivers and two transmitters. The MAX3222E features a 1µA shutdown mode that reduces power consumption in battery-powered portable systems. The MAX3222E receivers remain active in shutdown mode, allowing monitoring of external devices while consuming only 1µA of supply current. The MAX3222E and MAX3232E are pin, package, and functionally compatible with the industry-standard MAX242 and MAX232, respectively. The MAX3241E/MAX3246E are complete serial ports (three drivers/five receivers) designed for notebook and subnotebook computers. The MAX3237E (five drivers/three receivers) is ideal for peripheral applications that require fast data transfer. These devices feature a shutdown mode in which all receivers remain active, while consuming only 1µA (MAX3241E/MAX3246E) or 10nA (MAX3237E). The MAX3222E, MAX3232E, and MAX3241E are available in space-saving SO, SSOP, TQFN and TSSOP packages. The MAX3237E is offered in an SSOP package. The MAX3246E is offered in the ultra-small 6 x 6 UCSP(tm) package.



II. Manufacturing Information

A. Description/Function: ±15kV ESD-Protected, Down to 10nA, 3.0V to 5.5V, Up to 1Mbps, True

RS-232 Transceivers

B. Process:

C. Number of Device Transistors:

D. Fabrication Location: Oregon

E. Assembly Location: Malaysia, Philippines F. Date of Initial Production: January 24, 1998

III. Packaging Information

A. Package Type: 20-pin SSOP B. Lead Frame: Copper

C. Lead Finish: 100% matte Tin D. Die Attach: Conductive Epoxy E. Bondwire: Gold (1.3 mil dia.) F. Mold Material: Epoxy with silica filler G. Assembly Diagram: #05-1901-0177 H. Flammability Rating: Class UL94-V0 Level 1

I. Classification of Moisture Sensitivity per

JEDEC standard J-STD-020-C

J. Single Layer Theta Ja: 125°C/W K. Single Layer Theta Jc: 33°C/W L. Multi Layer Theta Ja: 83°C/W

M. Multi Layer Theta Jc: 33°C/W

IV. Die Information

A. Dimensions: 91 X 159 mils

B. Passivation: Si₃N₄/SiO₂ (Silicon nitride/ Silicon dioxide)

AI/0.5%Cu with Ti/TiN Barrier C. Interconnect:

D. Backside Metallization: None

E. Minimum Metal Width: 3.0 microns (as drawn) F. Minimum Metal Spacing: 3.0 microns (as drawn)

G. Bondpad Dimensions: 5 mil. Sq. H. Isolation Dielectric: SiO₂ I. Die Separation Method: Wafer Saw



V. Quality Assurance Information

A. Quality Assurance Contacts: Ken Wendel (Director, Reliability Engineering)

Bryan Preeshl (Managing Director of QA)

B. Outgoing Inspection Level: 0.1% for all electrical parameters guaranteed by the Datasheet.

0.1% For all Visual Defects.

C. Observed Outgoing Defect Rate: < 50 ppmD. Sampling Plan: Mil-Std-105D

x = 2.26 F.I.T. (60% confidence level @ 25°C)

VI. Reliability Evaluation

A. Accelerated Life Test

The results of the 135°C biased (static) life test are shown in Table 1. Using these results, the Failure Rate (λ) is calculated as follows:

$$\lambda = \frac{1}{\text{MTTF}} = \frac{1.83}{192 \times 4340 \times 476 \times 2}$$
 (Chi square value for MTTF upper limit)
$$\lambda = 2.26 \times 10^{-9}$$
 (where 4340 = Temperature Acceleration factor assuming an activation energy of 0.8eV)

The following failure rate represents data collected from Maxim"s reliability monitor program. Maxim performs quarterly life test monitors on its processes. This data is published in the Reliability Report found at http://www.maxim-ic.com/qa/reliability/monitor.

Cumulative monitor data for the B3 Process results in a FIT Rate of 0.51 @ 25C and 8.79 @ 55C (0.8 eV, 60% UCL)

B. Moisture Resistance Tests

The industry standard 85°C/85%RH or HAST testing is monitored per device process once a quarter.

C. E.S.D. and Latch-Up Testing

The RS60 die type has been found to have all pins able to withstand a transient pulse of

ESD-HBM: +/- 2000V per JEDEC JESD22-A114 ESD-CDM: +/- 750V per JEDEC JESD22-C101

Latch-Up testing has shown that this device withstands a current of +/-250 mA.



Table 1Reliability Evaluation Test Results

MAX3222ECAP+

TEST ITEM	TEST CONDITION	FAILURE IDENTIFICATION	SAMPLE SIZE	NUMBER OF FAILURES	
Static Life Test (Note 1)				
	Ta = 135°C	DC Parameters	476	0	
	Biased	& functionality			
	Time = 192 hrs.				
Moisture Testing	(Note 2)				
HAST	Ta = 130°C	DC Parameters	77	0	
	RH = 85%	& functionality			
	Biased				
	Time = 96hrs.				
Mechanical Stres	s (Note 2)				
Temperature	-65°C/150°C	DC Parameters	77	0	
Cycle	1000 Cycles	& functionality			
	Method 1010				

Note 1: Life Test Data may represent plastic DIP qualification lots.

Note 2: Generic Package/Process data