

RELIABILITY REPORT
FOR
MAX221EUE+

PLASTIC ENCAPSULATED DEVICES

March 19, 2009

# **MAXIM INTEGRATED PRODUCTS**

120 SAN GABRIEL DR. SUNNYVALE, CA 94086

Approved by	
Ken Wendel	
Quality Assurance	
Director, Reliability Engineering	



#### Conclusion

The MAX221EUE+ successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim's quality and reliability standards.

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## I. Device Description

#### A. General

The MAX221 is a +5V-powered, single transmit/receive RS-232 and V.28 communications interface with automatic shutdown/wake-up features and high data rate capabilities. The MAX221 achieves a low 1µA supply current with Maxim's revolutionary AutoShutdown™ feature. AutoShutdown saves power without changes to the existing BIOS or operating system by entering low-power shutdown mode when the RS-232 cable is disconnected, or when the transmitter of the connected peripheral is off. The MAX221 wakes up and drives the active-low INVALID pin high when an active RS-232 cable is connected, signaling the host that a peripheral is connected to the communications port. The MAX221 is available in a 16-pin SSOP package as well as a 16-pin TSSOP that uses 50% less board space than a 16-pin SO. For an equivalent RS-232 transceiver with enhanced ±15kV ESD protection, refer to the MAX221E data sheet.



## II. Manufacturing Information

A. Description/Function: +5V, 1μA, Single RS-232 Transceiver with AutoShutdown

B. Process: M5

C. Number of Device Transistors:

D. Fabrication Location: Oregon

E. Assembly Location: ATP Philippines, UTL Thailand, Carsem Malaysia

F. Date of Initial Production: July 24, 1999

#### III. Packaging Information

A. Package Type: 16-pin TSSOP
B. Lead Frame: Copper

C. Lead Finish:

D. Die Attach:

Conductive Epoxy

E. Bondwire:

Gold (1 mil dia.)

F. Mold Material:

G. Assembly Diagram:

#05-1901-0235

H. Flammability Rating:

Class UL94-V0

I. Classification of Moisture Sensitivity per Level 1

JEDEC standard J-STD-020-C

J. Single Layer Theta Ja: 106°C/W
K. Single Layer Theta Jc: 27°C/W
L. Multi Layer Theta Ja: 90°C/W
M. Multi Layer Theta Jc: 27°C/W

#### IV. Die Information

A. Dimensions: 87 X 105 mils

B. Passivation: Si<sub>3</sub>N<sub>4</sub>/SiO<sub>2</sub> (Silicon nitride/ Silicon dioxide

C. Interconnect: Aluminum/Si (Si = 1%)

D. Backside Metallization: None

E. Minimum Metal Width: Metal1 = 0.5 / Metal2 = 0.6 / Metal3 = 0.6 microns (as drawn)
 F. Minimum Metal Spacing: Metal1 = 0.45 / Metal2 = 0.5 / Metal3 = 0.6 microns (as drawn)

G. Bondpad Dimensions: 5 mil. Sq.
 H. Isolation Dielectric: SiO<sub>2</sub>
 I. Die Separation Method: Wafer Saw



## V. Quality Assurance Information

A. Quality Assurance Contacts: Ken Wendel (Director, Reliability Engineering)

Bryan Preeshl (Managing Director of QA)

B. Outgoing Inspection Level: 0.1% for all electrical parameters guaranteed by the Datasheet.

0.1% For all Visual Defects.

C. Observed Outgoing Defect Rate: < 50 ppm</li>D. Sampling Plan: Mil-Std-105D

#### VI. Reliability Evaluation

#### A. Accelerated Life Test

The results of the 135°C biased (static) life test are shown in Table 1. Using these results, the Failure Rate ( $\lambda$ ) is calculated as follows:

$$\lambda = 1 \over \text{MTTF}$$
 = 1.83 (Chi square value for MTTF upper limit)  
192 x 4340 x 80 x 2 (where 4340 = Temperature Acceleration factor assuming an activation energy of 0.8eV)

$$x = 13.4 \times 10^{-9}$$

% = 13.4 F.I.T. (60% confidence level @ 25°C)

The following failure rate represents data collected from Maxim's reliability monitor program. Maxim performs quarterly 1000 hour life test monitors on its processes. This data is published in the Product Reliability Report found at http://www.maxim-ic.com/. Current monitor data for the M5 Process results in a FIT Rate of 3.2 @ 25C and 54.8 @ 55C (0.8 eV, 60% UCL)

#### B. Moisture Resistance Tests

The industry standard 85°C/85%RH or HAST testing is monitored per device process once a quarter.

## C. E.S.D. and Latch-Up Testing

The RS72 die type has been found to have all pins able to withstand a HBM transient pulse of +/-2000 V per Mil-Std 883 Method 3015.7. Latch-Up testing has shown that this device withstands a current of +/-200 mA.



# Table 1

# Reliability Evaluation Test Results

# MAX221EUE+

TEST ITEM	TEST CONDITION	FAILURE IDENTIFICATION	SAMPLE SIZE	NUMBER OF FAILURES	
Static Life Test (	Note 1)				
,	Ta = 135°C Biased Time = 192 hrs.	DC Parameters & functionality	80	0	
Moisture Testing	(Note 2)				
85/85	Ta = 85°C RH = 85% Biased Time = 1000hrs.	DC Parameters & functionality	77	0	
Mechanical Stress	s (Note 2)				
Temperature	-65°C/150°C	DC Parameters	77	0	
Cycle	1000 Cycles Method 1010	& functionality			

Note 1: Life Test Data may represent plastic DIP qualification lots.

Note 2: Generic Package/Process data