

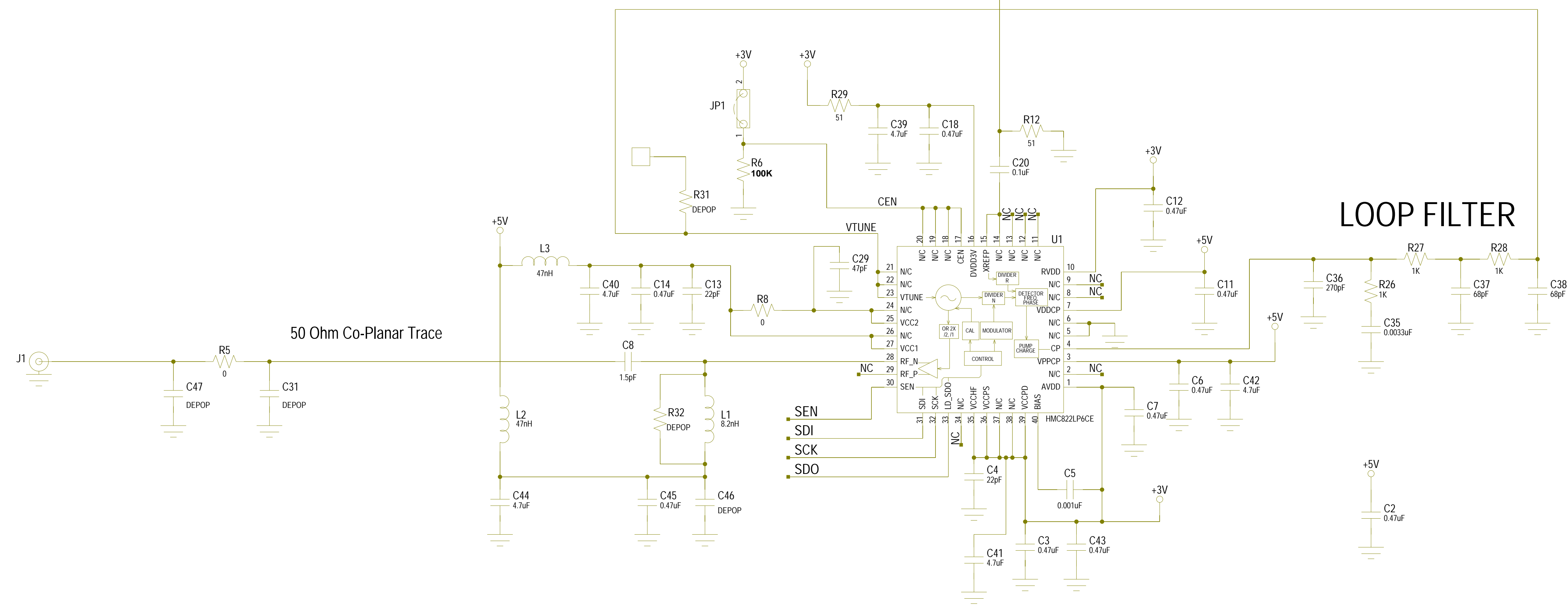
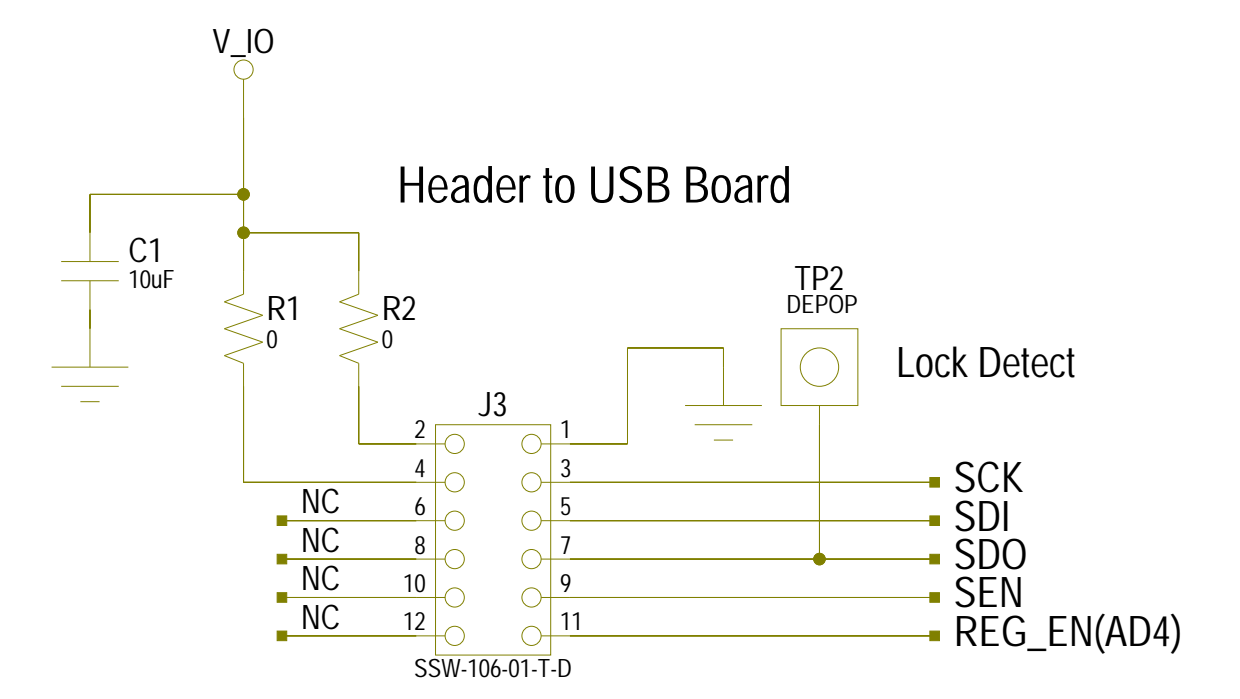
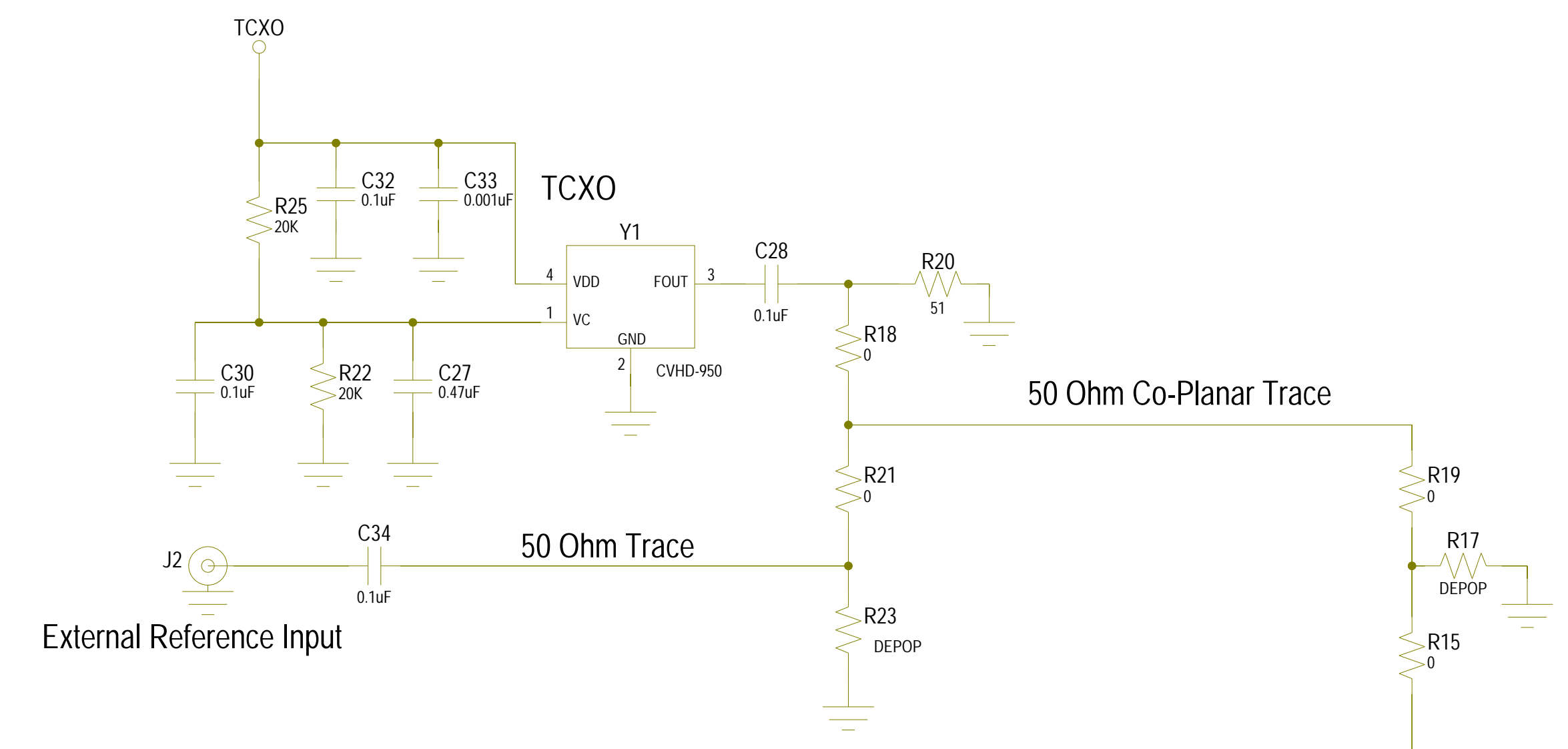
Analog Devices Welcomes Hittite Microwave Corporation

NO CONTENT ON THE ATTACHED DOCUMENT HAS CHANGED

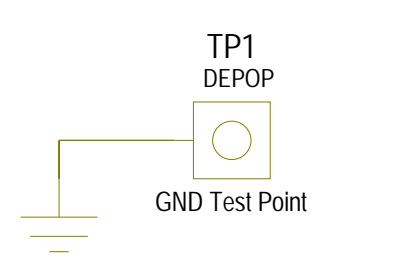


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REVISIONS					
REV	ECN#	ZONE	DESCRIPTION	NAME	DATE
A	20100115	PRODUCTION RELEASE PER ECN 20100115	D. YOUNG	24/01/10
B	CP121275	PRODUCTION CHANGE PER CP 121275	V. VADUVA	09/14/2012

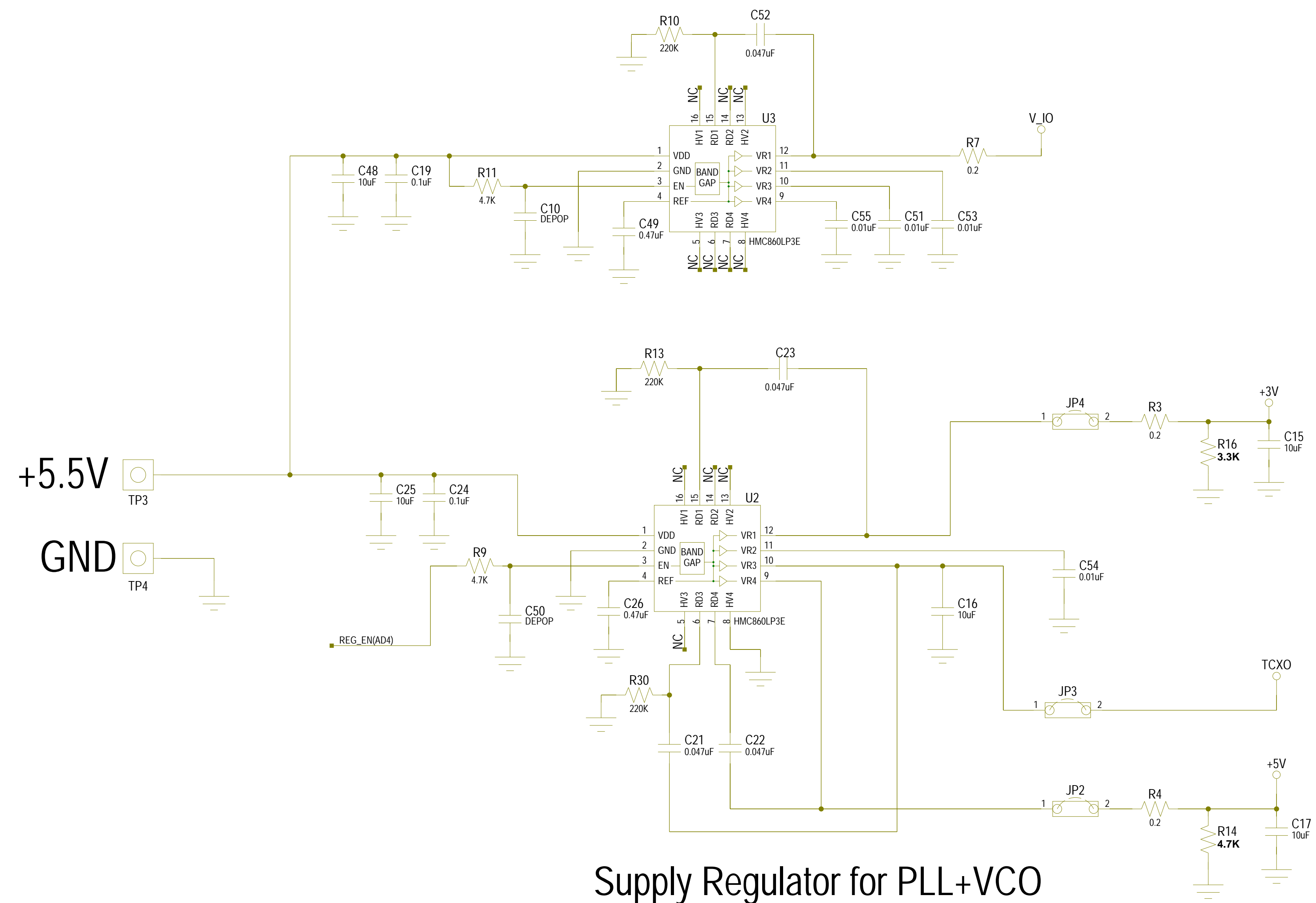


HMC822LP6CE Fo & Fo/2 Integrated PLL & VCO Eval



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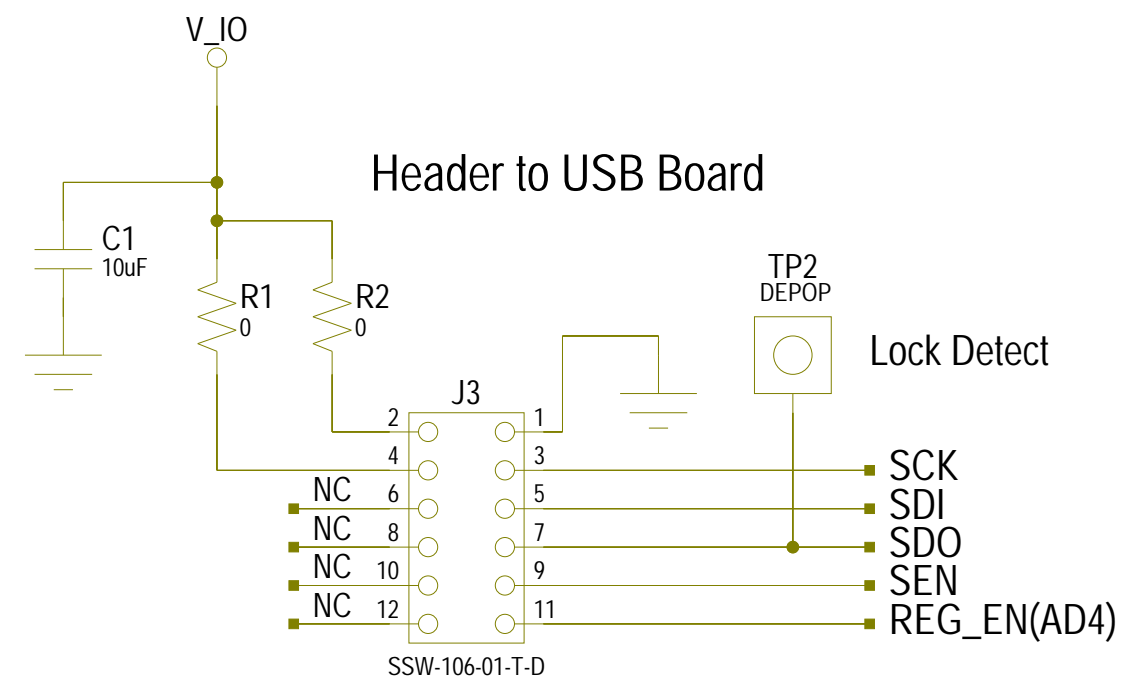
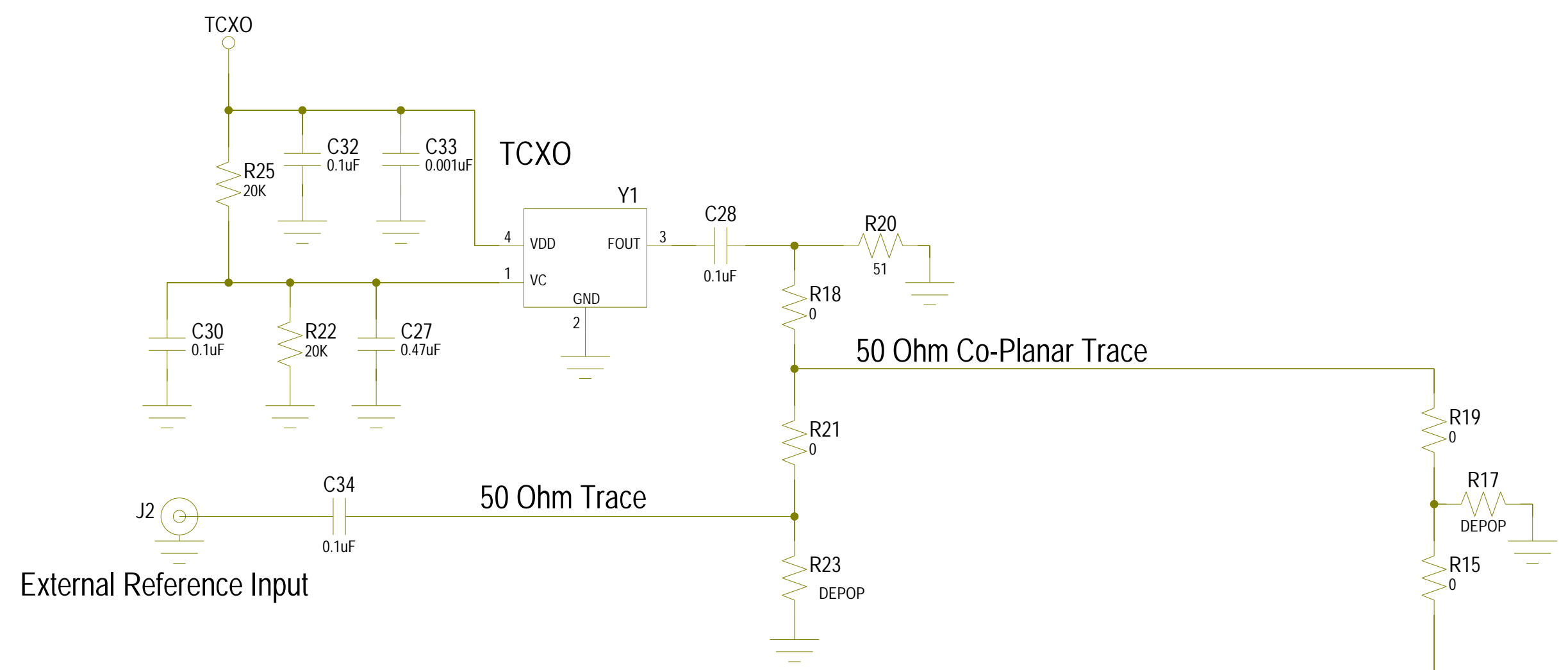
HITTITE MICROWAVE CORPORATION 20 Alpha Rd Chelmsford, MA 01824				
TITLE SCH, CUSTOMER EVALALUATION PLL & VCO FO & FO/2				
PROJECT DRAWING #: 129268				
DRAWN BY: D.YOUNG		DATE: 10/01/2010		SHEET: 1 OF 2
CODE ID NO: 1CN88		SIZE: D		REV: B



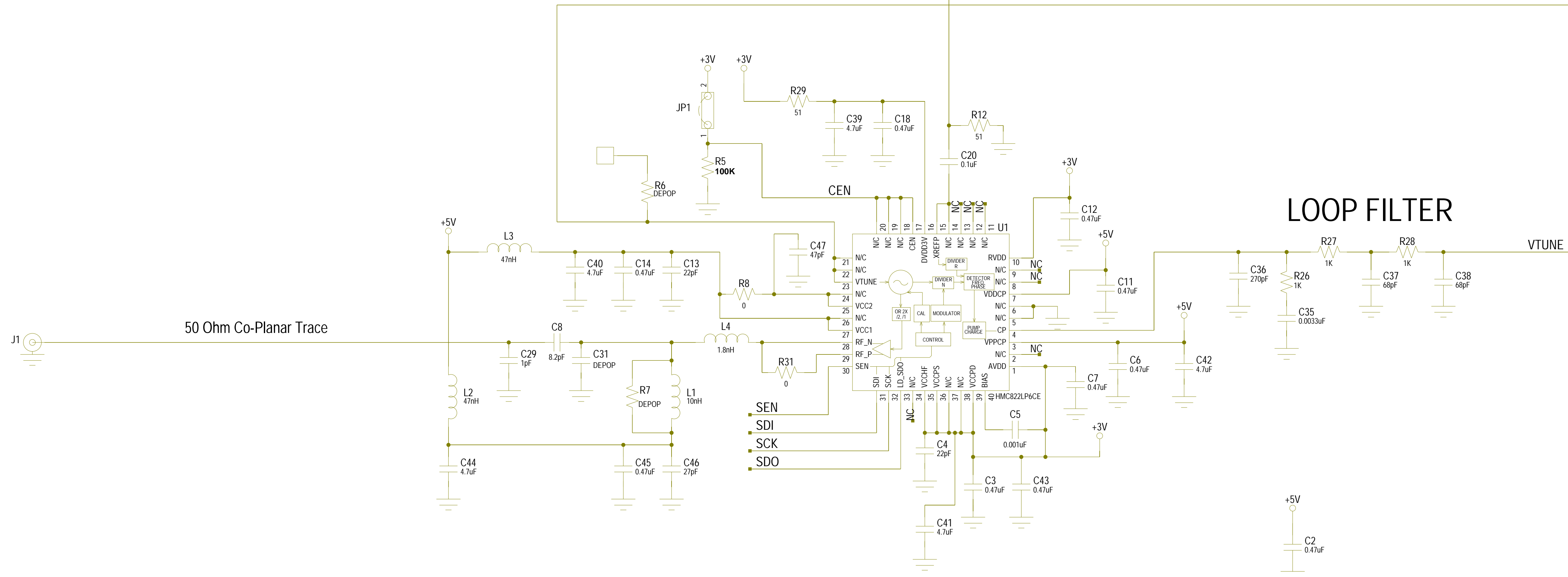
Supply Regulator for PLL+VCO

A recommended design practice is to connect the regulator Enable Pin #3 through a 4.7kOhm resistor to the system microcontroller/FPGA for power management control
 Small series resistors required in VR1 and VR4 output paths, as shown

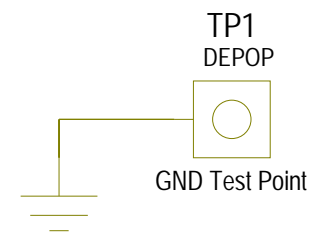
REVISIONS					
REV	ECN#	ZONE	DESCRIPTION	NAME	DATE
A	PRODUCTION RELEASE PER ECN 20100127	D. YOUNG	10/01/2010
B	CP121275	CHANGE RELEASE PER CP121275	V. VADUVA	09/14/2012



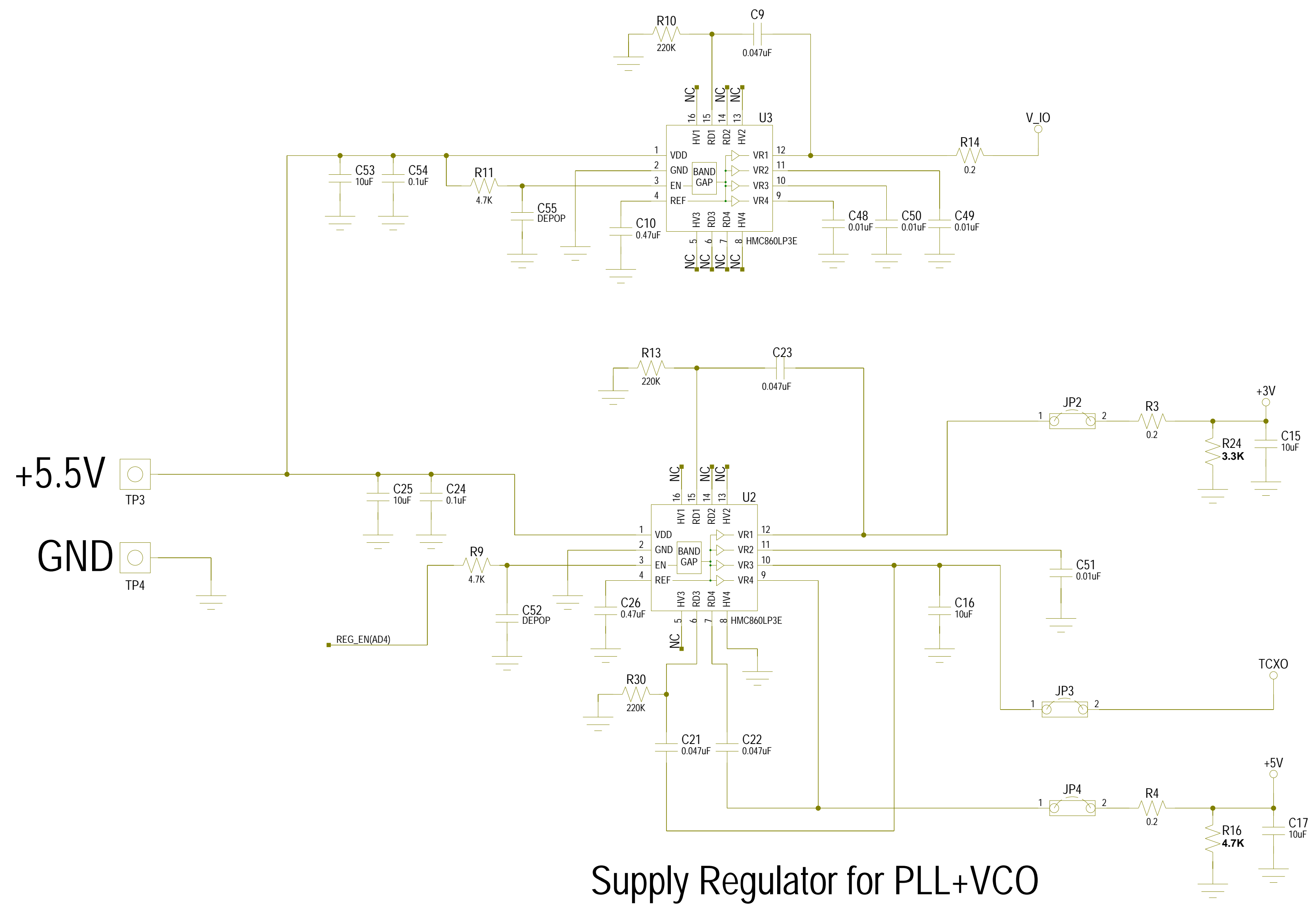
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HMC822 2xFO Integrated PLL & VCO Eval



HITTITE MICROWAVE CORPORATION 20 Alpha Rd Chelmsford, MA 01824	
TITLE SCH, CUSTOMER EVALUATION PLL & VCO 2 X FO	
PROJECT DRAWING #: 129275	
SHEET 1 OF 2 CODE ID NO. 1CN88 SIZE D REV B	DATE 10/01/2010 DRAWN BY D.YOUNG
22-11-2012_15:16	



Supply Regulator for PLL+VCO

A recommended design practice is to connect the regulator Enable Pin #3 through a 4.7kOhm resistor to the system microcontroller/FPGA for power management control
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