



MAX32675C ERRATA SHEET

Revision A3 Errata

The errata listed below describe situations where components of this revision perform differently than expected or differently than described in the data sheet. Analog Devices may, at its own discretion, take future steps to correct these errata when the opportunity to redesign the product presents itself. Prior to that, Analog Devices has determined the following potential workarounds that customers may want to consider when addressing one of the situations described below.

This errata sheet only applies to components of this revision. These components are branded on the topside of the package with a six-digit code in the form yywwRR, where yy and ww are two-digit numbers representing the year and work week of manufacture, respectively, and RR is the revision of the component. To obtain an errata sheet on other die revisions, visit the Analog Devices website at www.analog.com/MAX32675C.

1) SHORT RTS HART SIGNALS MAY CAUSE PERIPHERAL TO STOP OPERATION

Description:

Low pulses less than 1ms on the UART0_RTS pin used by the HART interface may place the HART peripheral in an unknown state. The only recovery from this state is to reset the peripheral in software. (1968)

Workaround:

The HART driver configures P1.8 as a GPIO to process the UART0_RTS signal. Enable a GPIO interrupt on P1.8 and use it to start a timer to measure the time between the falling and rising edges of the signal. If the width of the low pulse is less than 1ms, the HART peripheral must be reset by clearing and then setting `AFE_SYS_CTRL.hart_en`.

MAX32675C

REV A3 ERRATA

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	9/24	Initial release	—
1	11/24	Removed erratum #2	1

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