

SELECTED ITEM DRAWING



DC to 20 GHz, MZ Optical Modulator Driver

Unless otherwise specified DIMENSIONS ARE IN INCHES (MM)	TOLERANCES: .XX +/- 0.010 .XXX +/- 0.005 .XXXX +/- 0.002 ANGLES +/- .5 DEG	Drawing practices per ASME Y14.100	SIZE	DRAWING NUMBER
			A	SID000042

SID000042

Rev. A

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1. SCOPE

1.1. Scope

This drawing establishes the requirements for the non-hermetic, DC to 20 GHz, GaAs, PHEMT, MMIC, MZ Optical Modulator Driver, to be screened with guidelines to MIL-PRF-38535, Class Level S, to the requirements specified in 4.1, 4.2, and 4.3 herein.

1.2. Analog Devices Part Number

Generic Part Number

ADH870S

Screened Part Number

HMC6126LC5

2. APPLICABLE DOCUMENTS

2.1. Government Documents

Unless otherwise specified, the following drawings and standards, of the issue in effect on the date of the accepted purchase order, in the Department of Defense Index of Specifications and Standards (DODISS) and supplement thereto, shall form a part of this drawing to the extent specified herein.

DEPARTMENT OF DEFENSE TEST METHOD STANDARD

MIL-STD-883

Microcircuits

MIL-STD-1580C

Destructive Physical Analysis for Electronic, Electromagnetic, and Electromechanical Parts

DEPARTMENT OF DEFENSE PERFORMANCE SPECIFICATIONS

MIL-PRF-38535

Integrated Circuits (Microcircuits) Manufacturing, General Specification For

2.2. Non-Government Documents

The following documents, of the issue in effect on the date of the purchase order, form a part of this drawing to the extent specified herein:

Analog Devices Inc.

[ADI Standard Space Products Program](#) – ASD-*lite*.

HMC870LC5 Data Sheet

Commercial Product Datasheet

v05.0614 (Reference Only)

3. REQUIREMENTS

3.1. General Requirements

The devices delivered shall comply to this specification.

3.2. Design Construction and Physical Dimensions

The design construction and physical dimensions shall be as defined in Figure 1 herein.

3.3. Traceability

Each delivered device shall be traceable to a production lot. Inspection lot records shall be maintained to provide traceability of its origin.

3.4. DPA

If specified on Purchase Order, DPA testing shall be done in accordance with MIL-STD-1580C, Sections 16.1.

3.5. Burn-In and Life Test Circuit

The burn-in and life test circuit and conditions shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test methods 1005 and 1015 per MIL-STD-883.

4. QUALITY ASSURANCE PROVISIONS

4.1. Wafer Lot Acceptance Testing

Not applicable.

4.2. Flight Screening Requirements

Flight screening requirements shall be per MIL-STD-883 TM 5004 for Class level S microcircuits modified for non-hermetic packaged MMIC devices.

4.2.1. Electrical Test Requirements

Electrical test requirements are defined in Table I herein.

4.2.2. Electrical Performance Characteristics

Electrical performance characteristics are specified on Table II herein.

4.2.3. Burn-In Delta Requirements

Pre and Post Burn-In Electrical test and delta parameters shall consist of the tests specified in Table III herein.

4.3. Quality Conformance Inspection (QCI)

Group B and Group D tests shall be performed per MIL-STD-883 TM 5005 for Class level S microcircuits modified for non-hermetic packaged MMIC devices.

4.3.1. Post Steady State Life Electrical Test

Post steady state life electrical tests shall consist of the tests specified per Table II tested at room temperature only. Devices must meet delta parameter requirements in accordance with Table III herein.

5. MIL-PRF-38535 ASD-LITE EXCEPTIONS

The manufacturing flow described in the ADI STANDARD SPACE PRODUCTS PROGRAM is to be considered a part of this specification.

5.1. Wafer Fabrication

Foundry information is available upon request.

5.2. Flight Screening Flow

Wafer lot Acceptance: Not applicable for non-hermetic air cavity packages.

Non-destruct Bond Pull: Not applicable for non-hermetic air cavity packages.

Internal visual inspection: DPA performed in lieu of visual – commercial criteria is applicable.

Seal, Fine Leak: Not applicable for non-hermetic air cavity packages.

Seal, Gross Leak: Not applicable for non-hermetic air cavity packages.

5.3. Group B

Subgroup 2: Resistance to solvents is not applicable.

Subgroup 3: Steam aging shall not be required for pre-conditioning.

Subgroup 4: Not applicable for non-hermetic air cavity packages.

Subgroup 5: Qty/Acc = 10/0.

Subgroup 6: Qty/Acc = 6/0.

5.4. Group D

Subgroup 2: Not applicable for non-hermetic air cavity packages.

Subgroup 5: Not Applicable for non-hermetic air cavity packages.

Subgroup 6: Not applicable for non-hermetic air cavity packages.

Subgroup 7: Not applicable for non-hermetic air cavity packages.

Subgroup 8: Not applicable for non-hermetic air cavity packages.

Subgroup 9: Not applicable for non-hermetic air cavity packages.

6. PREPARATION FOR DELIVERY

The preparation for delivery, packaging, preservation, ESD protection and handling shall be in accordance with MIL-PRF-38535.

6.1. Part Marking

Devices shall be marked as specified on Figure 1 herein.

6.2. Inspection Data Requirements

The following data shall accompany each shipment.

- a. A Certificate of Conformance (C of C) certifies that the lot(s) meets all requirements of this specification.
- b. DPA Report (if applicable)
- c. Summary of electrical test requirements defined in 4.2 herein.
- d. Summary of QCI results defined in 4.3 herein.
- e. Failure Analysis with photos (If applicable)
- f. A cover sheet indicating the following purchasing information:
 1. Customer purchase order number.
 2. Analog Devices part number.
 3. Part lot identification codes.
 4. Date & quantity shipped.

TABLE I: ELECTRICAL TEST REQUIREMENTS

Test Requirement	Subgroups (in accordance with MIL-PRF-38535, Table III)
Interim Electrical Parameters	1, 4, 7
Final Electrical Parameters	1, 4, 7 <u>1/2/</u>
Group A Electrical Parameters	1, 2, 3, 4, 5, 6, 7, 8A, 8B
Group B End-Point Electrical Parameters	1, 4, 7 <u>2/</u>
Group D End-Point Electrical Parameters	1, 4, 7

TABLE I Notes:

1/ PDA applies to Table I subgroup 1 and Table III delta parameters.

2/ See Table III for delta parameters

TABLE II: ELECTRICAL PERFORMANCE CHARACTERISTICS (-40 °C, +25 °C AND +85 °C)

Parameter	Test Conditions <u>1/2/3/</u> Unless otherwise specified	Group A Subgroups	Limits			Units
			Min	Typ	Max	
Gain	P _{RFIN} = -10 dBm	4	13			dB
Output Power for 1 dB Compression (OP1dB) <u>4/</u>		7	19			dBm
Supply Current (I _{dd}) <u>5/</u>	No Signal at RFIN	1		165		mA

TABLE II Notes:

1/ Test limits apply at +25 °C with V_{dd} = 7V, V_{ctl} = 1V, and I_{dd} = 165 mA.

2/ Electrical tests measurements made at 10 GHz only.

3/ -40 °C and +85 °C (subgroups 2, 3, 5, 6, 8A, and 8B) are Read and Record only.

4/ OP1dB test is a go-no-go confirming minimum requirements is met.

5/ Adjust V_{gg} between -2V and 0V to achieve I_{dd} = 165 mA typical at -40 °C, +25 °C, and +85 °C.

TABLE III: BURN-IN/LIFE TEST DELTA LIMITS 1/2/3/

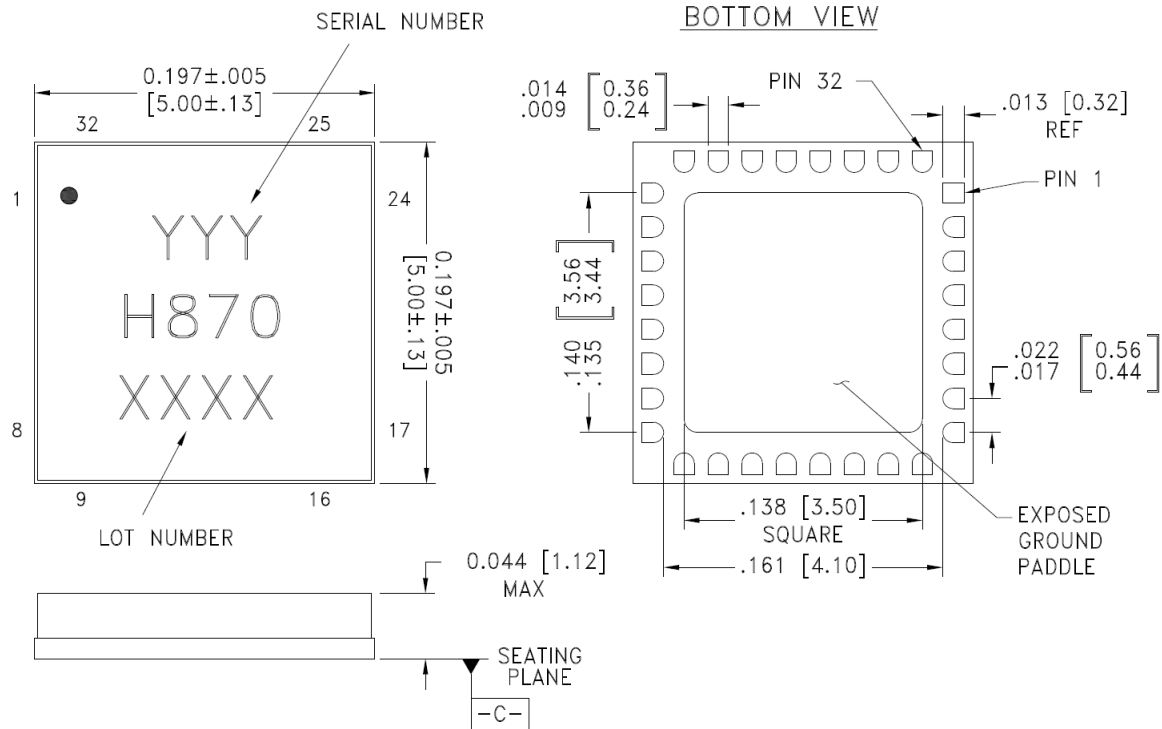
Parameter	Test Conditions	Delta Limits	Units
Gain	Per Table II	± 1	dB
Supply Current (I _{dd})		± 10	%

TABLE III Notes:

1/ Delta test is performed at T_A = +25 °C only.

2/ Table II limits will not be exceeded.

3/ Deltas calculated at pre/post 240 hours and post 240 hours / post 1000 hours.



NOTES:

1. PACKAGE BODY MATERIAL: ALUMINA
2. LEAD AND GROUND PADDLE PLATING: 30-80 MICROINCHES GOLD OVER 50 MICROINCHES MINIMUM NICKEL.
3. DIMENSIONS ARE IN INCHES [MILLIMETERS].
4. LEAD SPACING TOLERANCE IS NON-CUMULATIVE.
5. CHARACTERS TO BE INK OR LASER MARKED WITH .018" MIN TO .030" MAX HEIGHT REQUIREMENTS. UTILIZE MAXIMUM CHARACTER HEIGHT BASED ON LID DIMENSIONS AND BEST FIT. LOCATE APPROX. AS SHOWN.
6. PACKAGE WARP SHALL NOT EXCEED 0.05mm DATUM -C-
7. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.

PIN #	FUNCTION	PIN #	FUNCTION	PIN #	FUNCTION	PIN #	FUNCTION
1	N/C	9	N/C	17	N/C	25	N/C
2	V _{CTL}	10	N/C	18	N/C	26	N/C
3	N/C	11	N/C	19	N/C	27	N/C
4	N/C	12	N/C	20	GND	28	N/C
5	RFIN	13	V _{gg}	21	RFOUT & V _{dd}	29	ACG2
6	GND	14	N/C	22	N/C	30	ACG1
7	N/C	15	ACG4	23	N/C	31	N/C
8	N/C	16	ACG3	24	N/C	32	N/C

The HMC6126LC5 has a MSL rating of MSL3.

Figure 1 – Device Outline for the HMC6126LC5

ORDERING GUIDE

Model	Temperature Range	Package Description	Package Option
HMC6126LC5	-40 °C to +85 °C	32-Lead Ceramic Leadless Chip Carrier	LCC (E-32-1)

Revision History

Revision History		
Rev	Description of Change	Date
A	Initial release.	10/24/2024