

PCN # 1712

DATE: June 13, 2017

EXPECTED PCN SHIP DATE: June 13, 2017



Quality Assurance  
160 Rio Robles  
San Jose, CA 95134

www.maximintegrated.com

PROCESS CHANGE NOTICE  
 PRODUCT CHANGE NOTICE

MAXIM INTEGRATED HEREBY ISSUES NOTIFICATION OF CHANGE  
THAT MAY AFFECT THE FOLLOWING CATEGORIES:

DESIGN     WAFER FAB     ASSEMBLY     TEST     ELEC/MECH SPECS

AFFECTED PRODUCT:

Ordering P/N: (See PN listing XLS in PCN ZIP file)

CHANGE FROM: 68 TQFN-EP pin configuration drawing, pin 46 is labeled VDDIO. The Pin description listing for pin 46 is VDDIOH.

CHANGE TO: 68 TQFN-EP pin configuration drawing pin 46 is changed to VDDIOH. The Pin description listing for pin 46 of VDDIOH is correct. Added Exposed Pad to pin description listing.

JUSTIFICATION: Correct pin configuration drawing to match pin description listing, Added Exposed Pad to pin description listing.

There are no changes to the form, fit, function or quality for this change.

Please see the attached marked up datasheet for all the changes

TRACEABILITY: Maxim Integrated maintains full traceability by device marking, packaging labels and shipment documents.

Maxim Integrated's Change Notification System is designed to keep our customer base apprised of major product, manufacturing, or facility improvements.

*Nasser Ali Chaouche*

Nasser AliChaouche / PCN Coordinator

For further information, please contact either of the people listed below.

Contact your local Maxim Integrated Company Representative

or

Nasser AliChaouche, PCN Coordinator

408-601-5660 / pcn.coordinator@maximintegrated.com