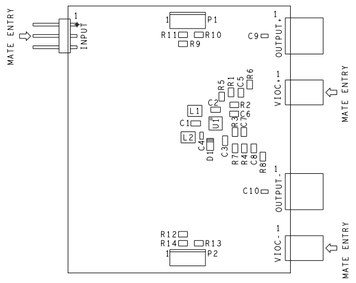
ART FILM - assy1

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	28JAN20	A. SERQUINA



ASSEMBLY NOTES:

1. BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).

2. REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.

3. REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.

4. RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).

PRIMARY SIDE

EVALUATION BOARD

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES .XX +-.010 +-1/32 +- 2 .XXX +-.005 .XXXX +-.0050	APPROVAL	DATE	ANALOG DEVICES	WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887
MATERIAL	TEMPLATE ENGINEER -	-	TITLE	ASSEMBLY SCP-LT3463 EVALZ
	HARDWARE SERVICES -	-		
	HARDWARE SYSTEMS -	-		
	TEST ENGINEER -	-		
	COMPONENT ENGINEER -	-		
	TEST PROCESS -	-		
	HARDWARE RELEASE -	-		
FINISH	DESIGNER L.EVANGELIO	19JAN20	SIZE / FSCM NO / DRAWING NUMBER / REV	C / 24355 / 01-060170 / A
	PTD ENGINEER A.SERQUINA	28JAN20		
	CHECKER -	-		
DO NOT SCALE DWG			SCALE 1/1	SHEET 1 OF 1

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