


REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	AUTHOR

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integrated™

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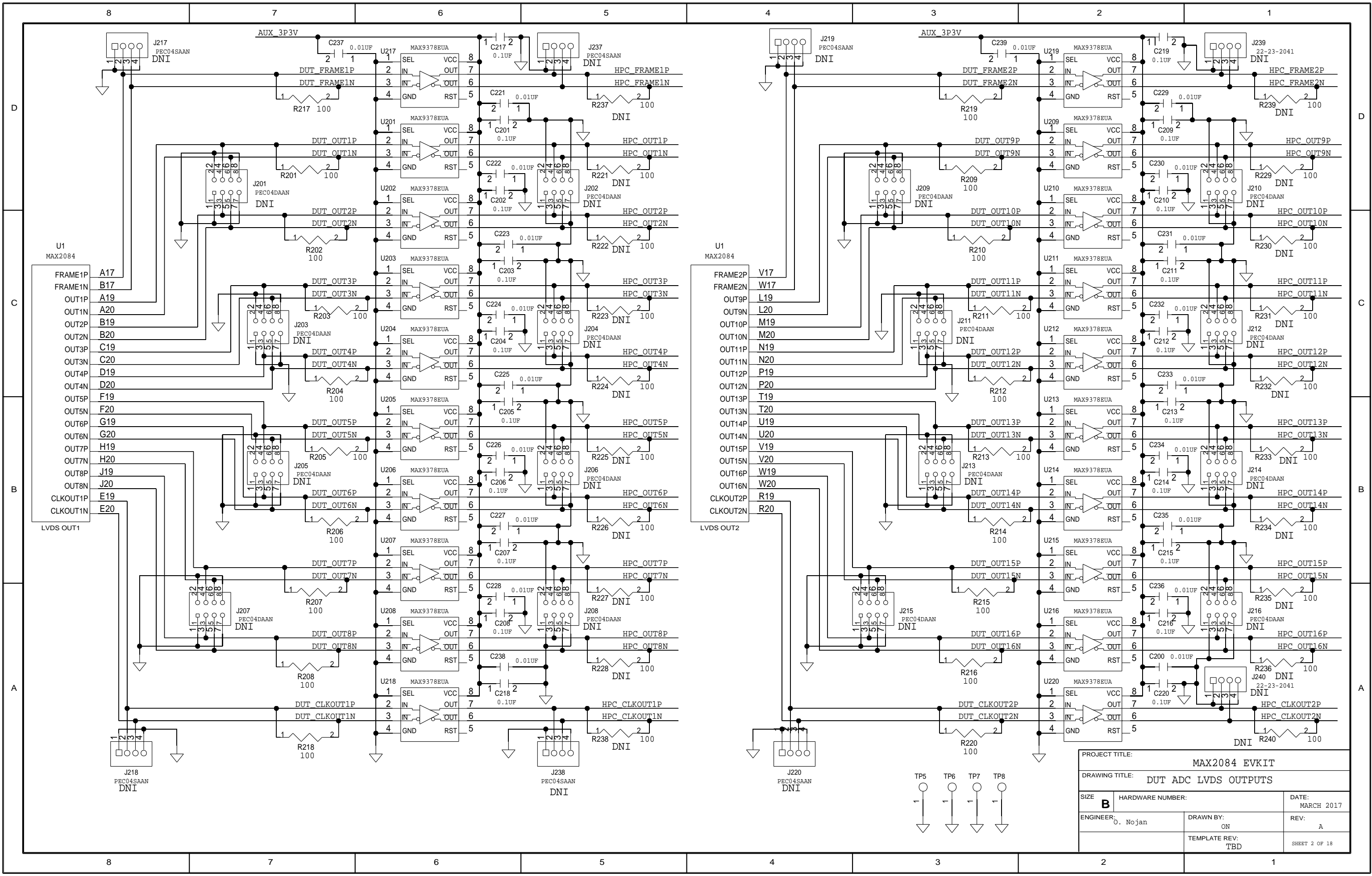
PROJECT TITLE:
MAX2084 EVKIT

DRAWING TITLE:
DUT TR SWITCH & ANALOG INPUTS

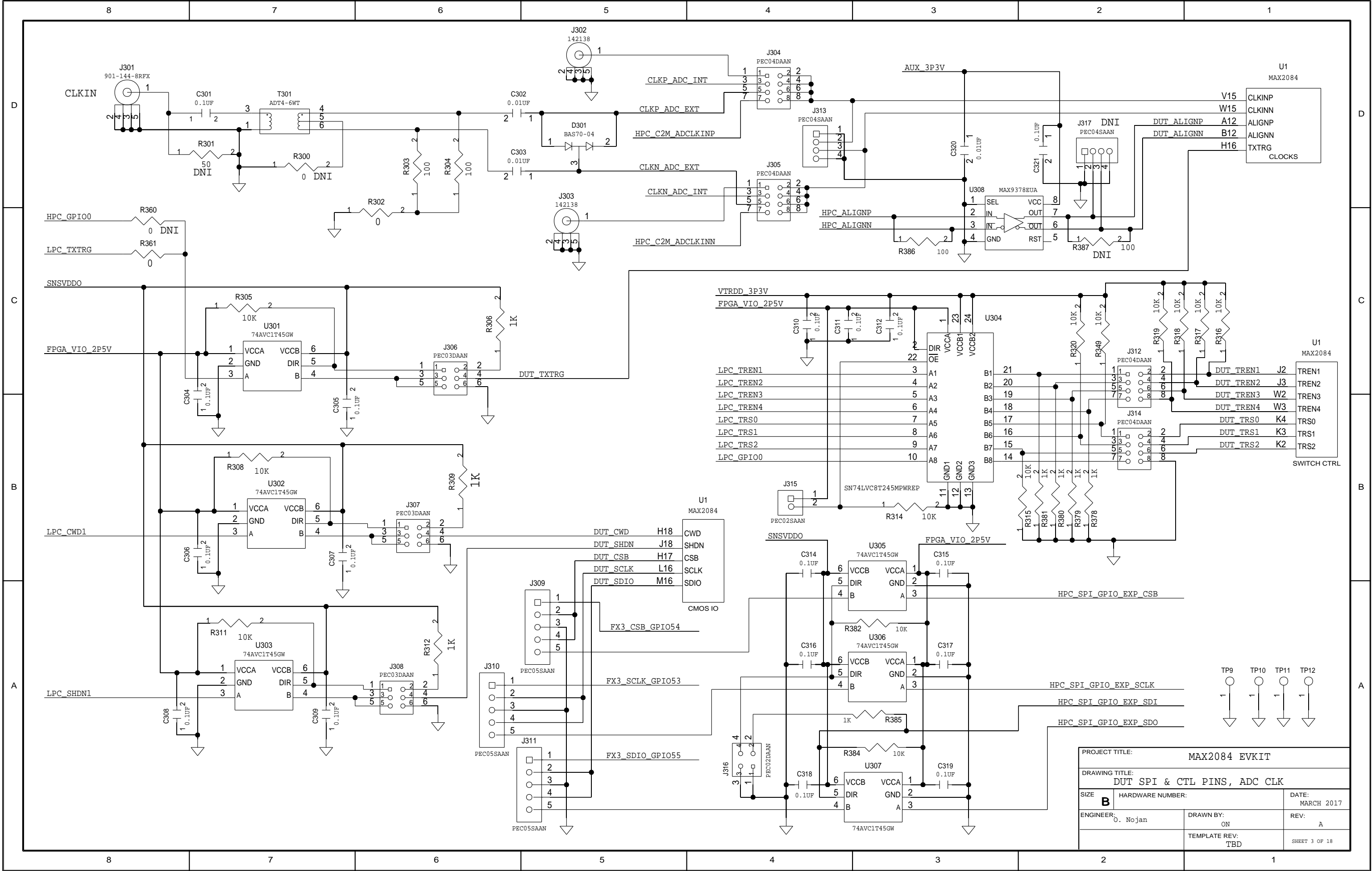
SIZE B	HARDWARE NUMBER:		
ENGINEER: O. Nojan	DRAWN BY: ON	DATE: MARCH 2017	
TEMPLATE REV: TBD		REV: A	

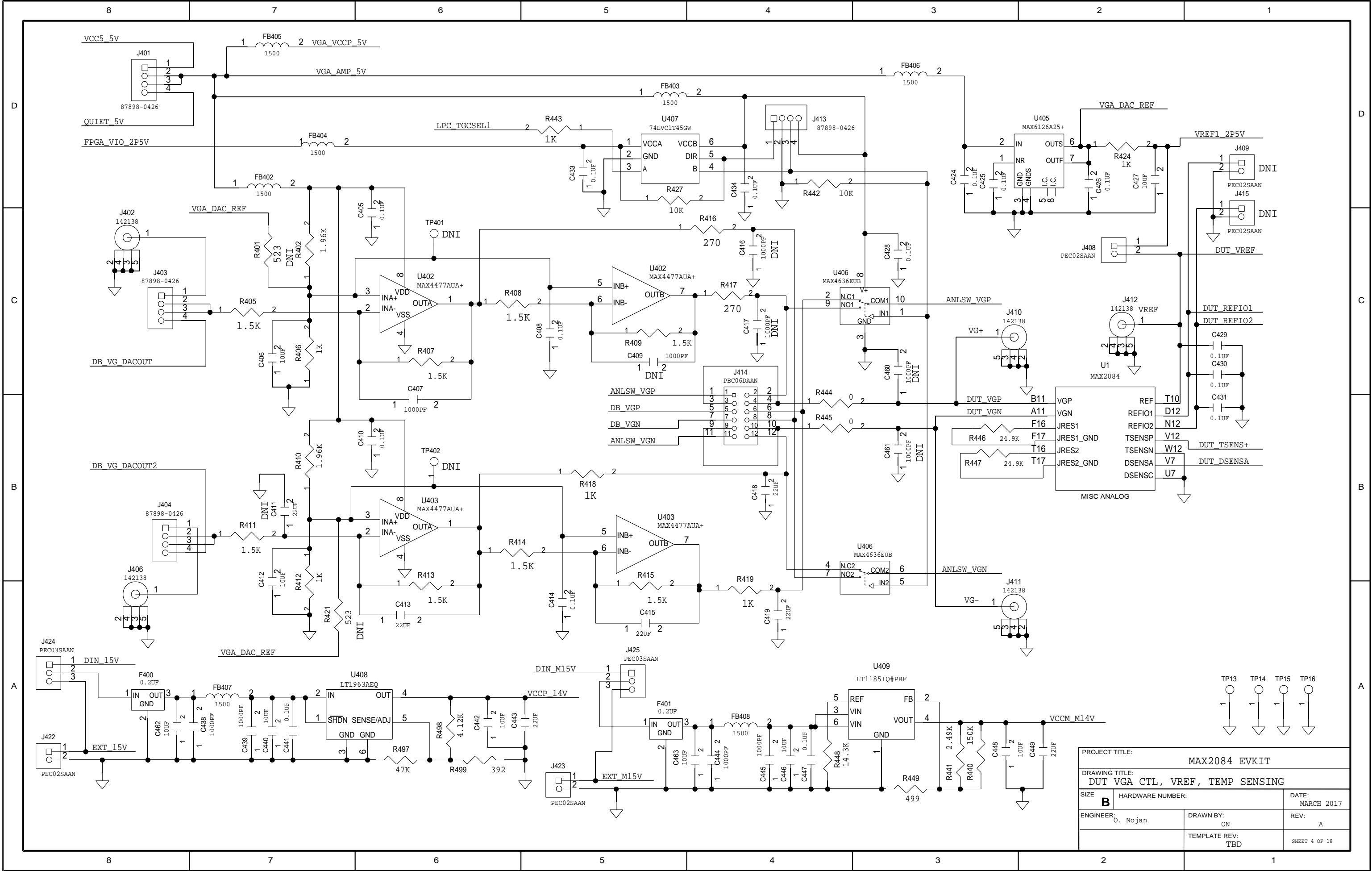
- U1
MAX2084
- C5 AG1
 - D5 AG2
 - G5 AG3
 - H5 AG4
 - M5 AG5
 - N5 AG6
 - T5 AG7
 - U5 AG8
- AG

- TP1 TP2 TP3 TP4
- TP30 TP31 TP32 TP33 TP34 TP35

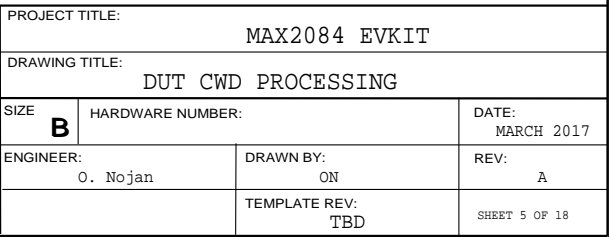


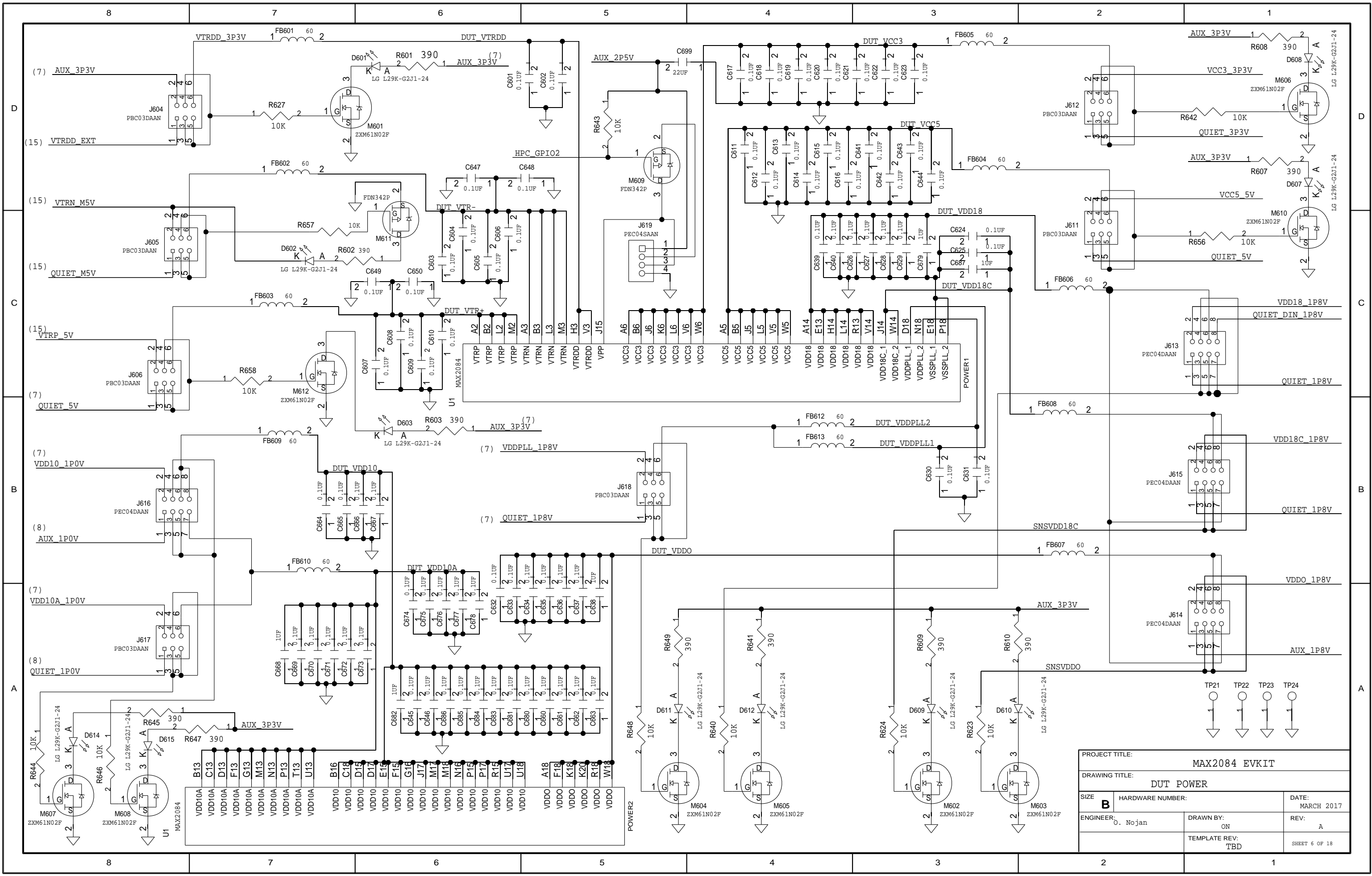
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DRAWING TITLE:		DUT ADC LVDS OUTPUTS	
SIZE	HARDWARE NUMBER:	DATE:	
B		MARCH 2017	
ENGINEER:	O. Nojan	DRAWN BY:	REV:
		ON	A
		TEMPLATE REV:	
		TBD	SHEET 2 OF 18



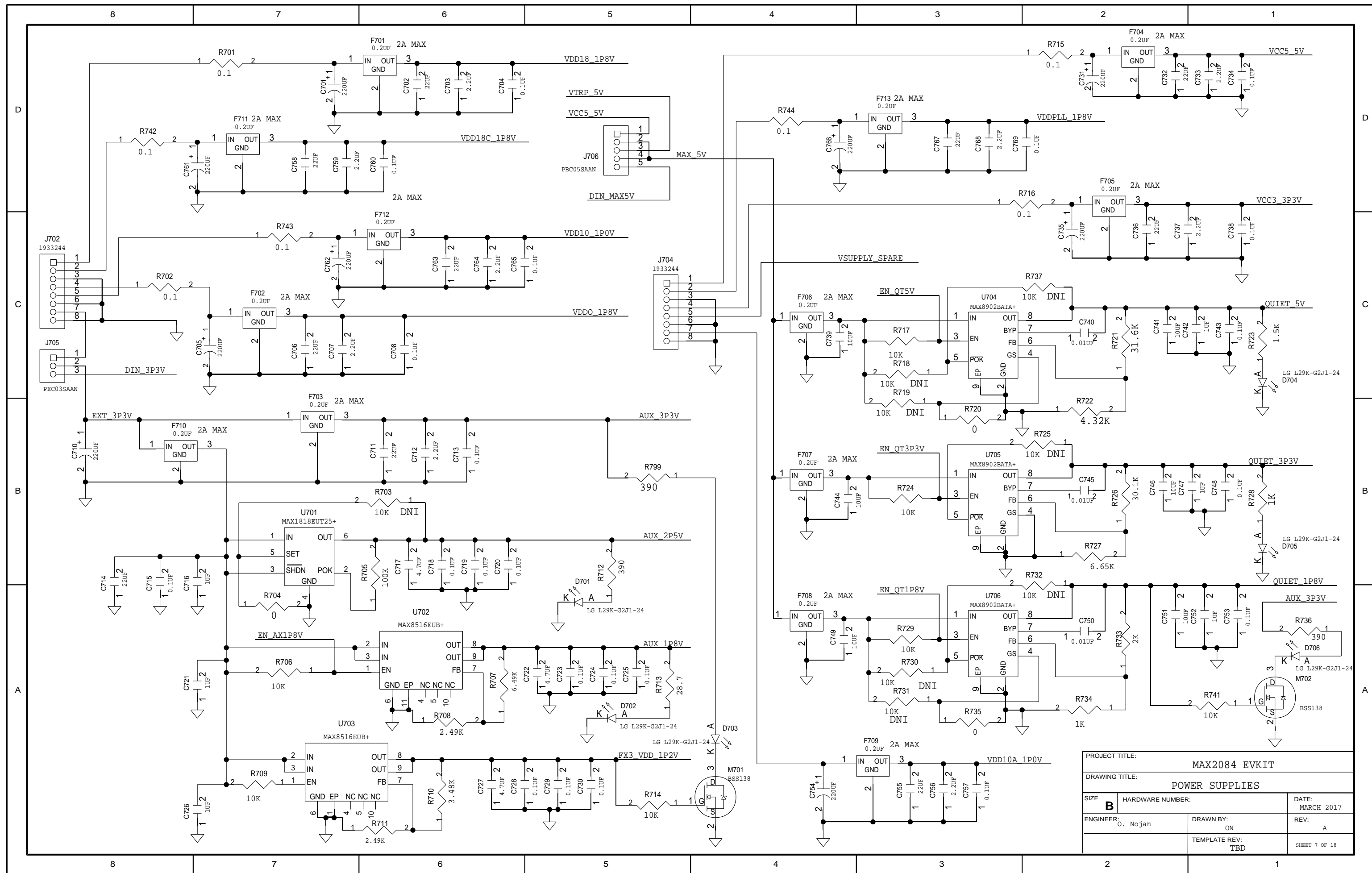


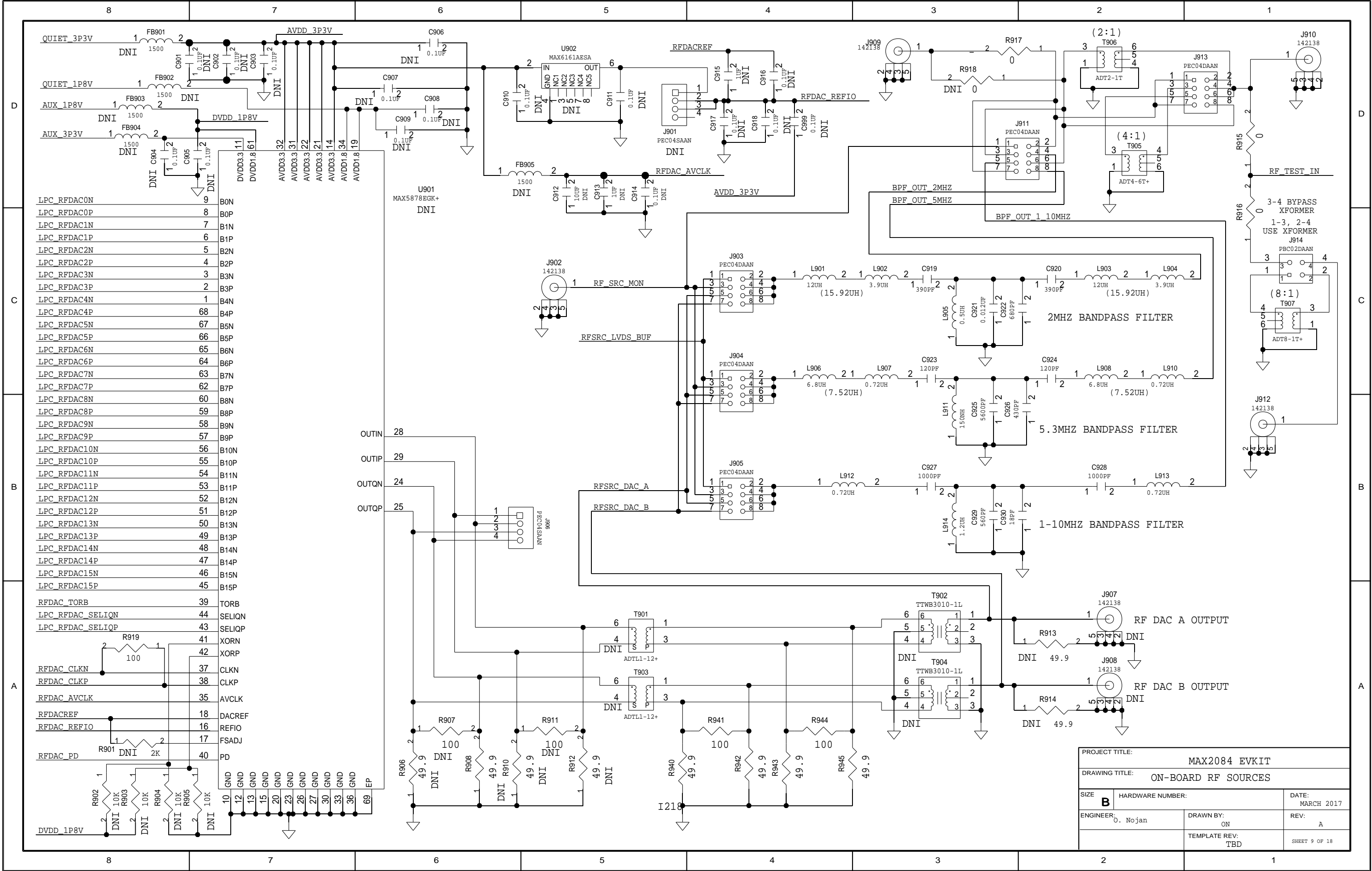
PROJECT TITLE:		
MAX2084 EVKIT		
DRAWING TITLE:		
DUT VGA CTL, VREF, TEMP SENSING		
SIZE	HARDWARE NUMBER:	DATE:
B		MARCH 2017
ENGINEER:	DRAWN BY:	REV:
O. Nojan	ON	A
	TEMPLATE REV:	
	TBD	
SHEET 4 OF 18		



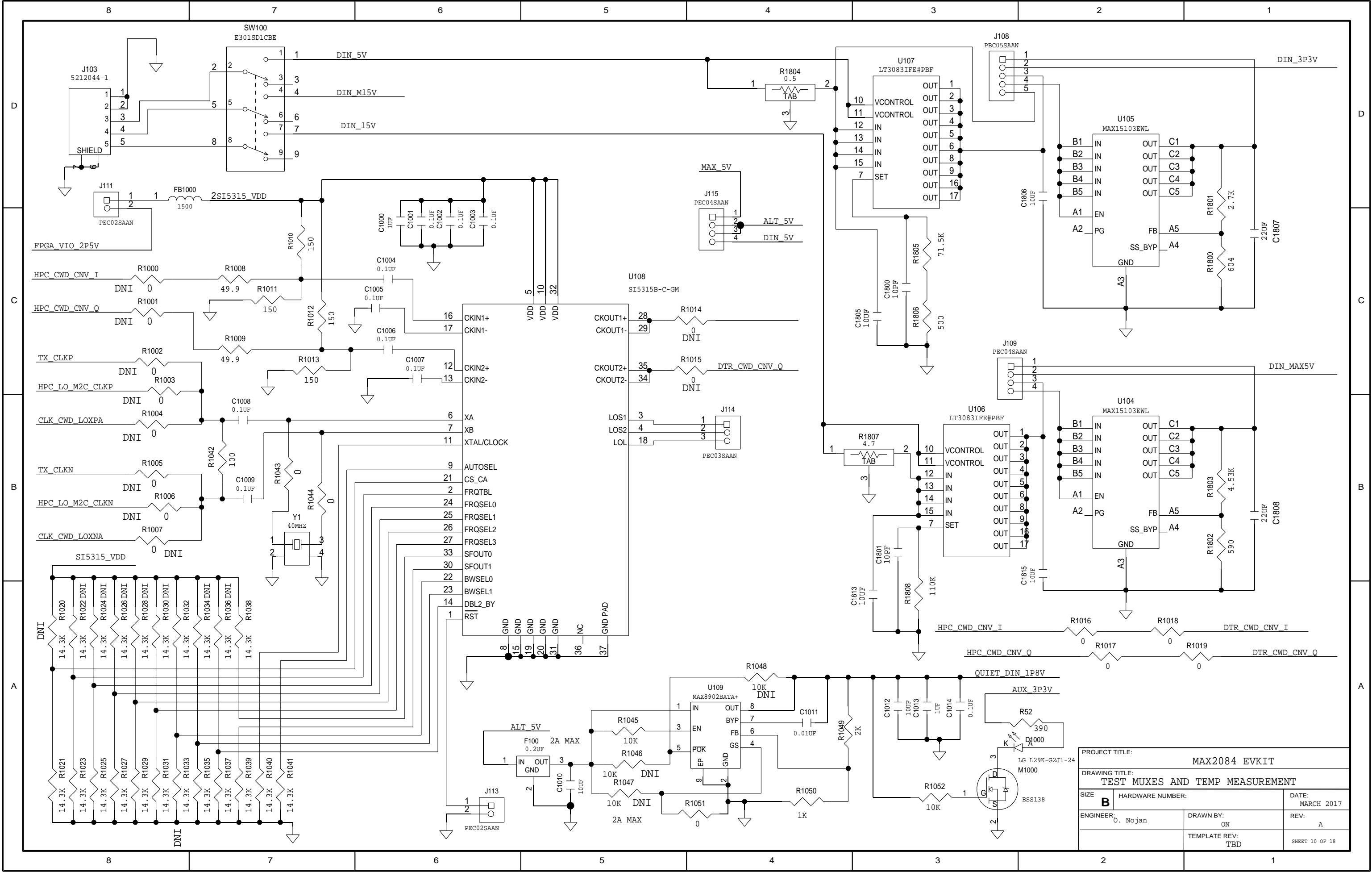


PROJECT TITLE:			MAX2084 EVKIT		
DRAWING TITLE:					
DUT POWER					
SIZE	HARDWARE NUMBER:			DATE:	
B				MARCH 2017	
ENGINEER:	O. Nojan		DRAWN BY:	REV:	
			ON	A	
			TEMPLATE REV:	SHEET 6 OF 18	
			TBD		

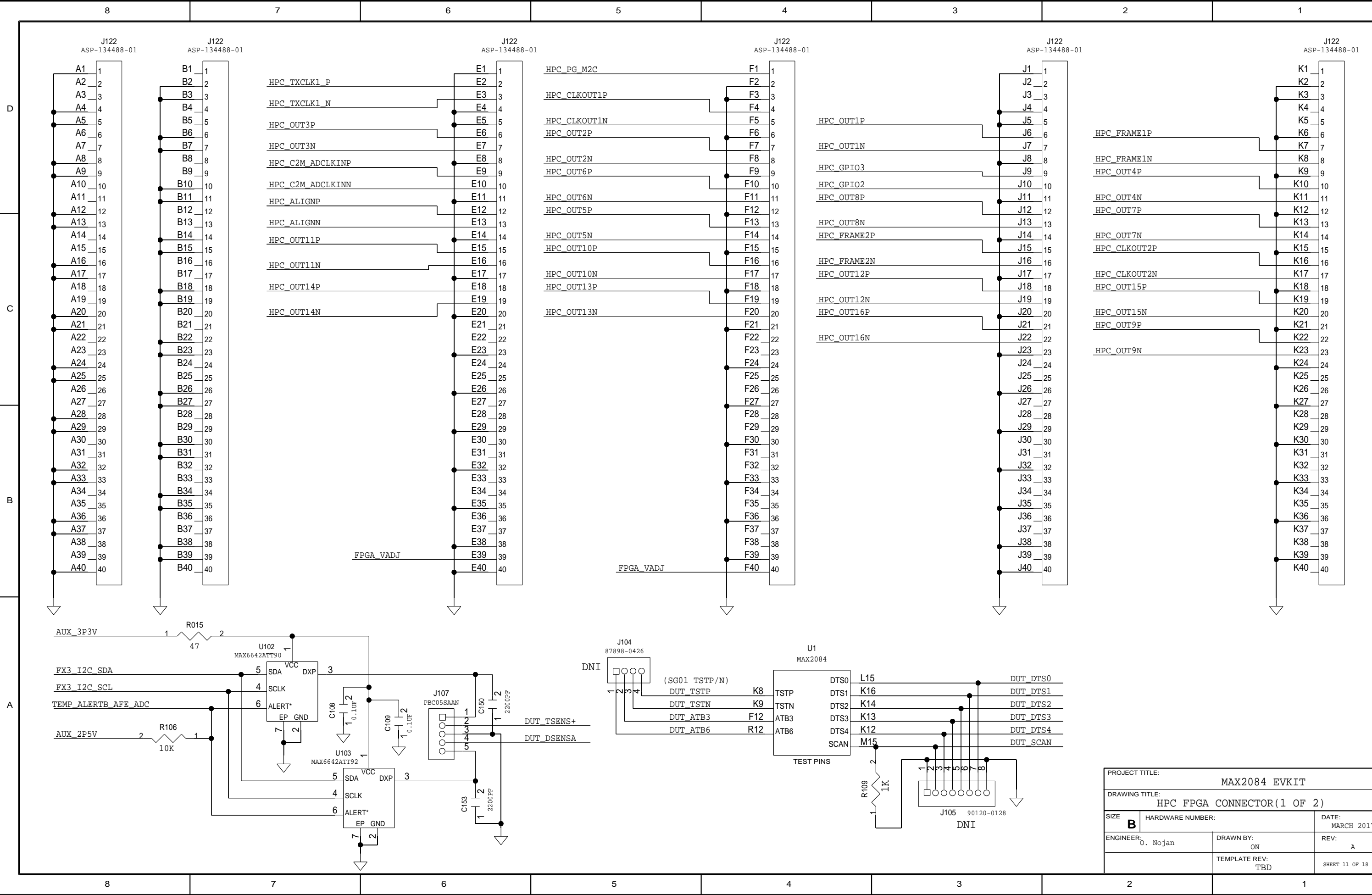




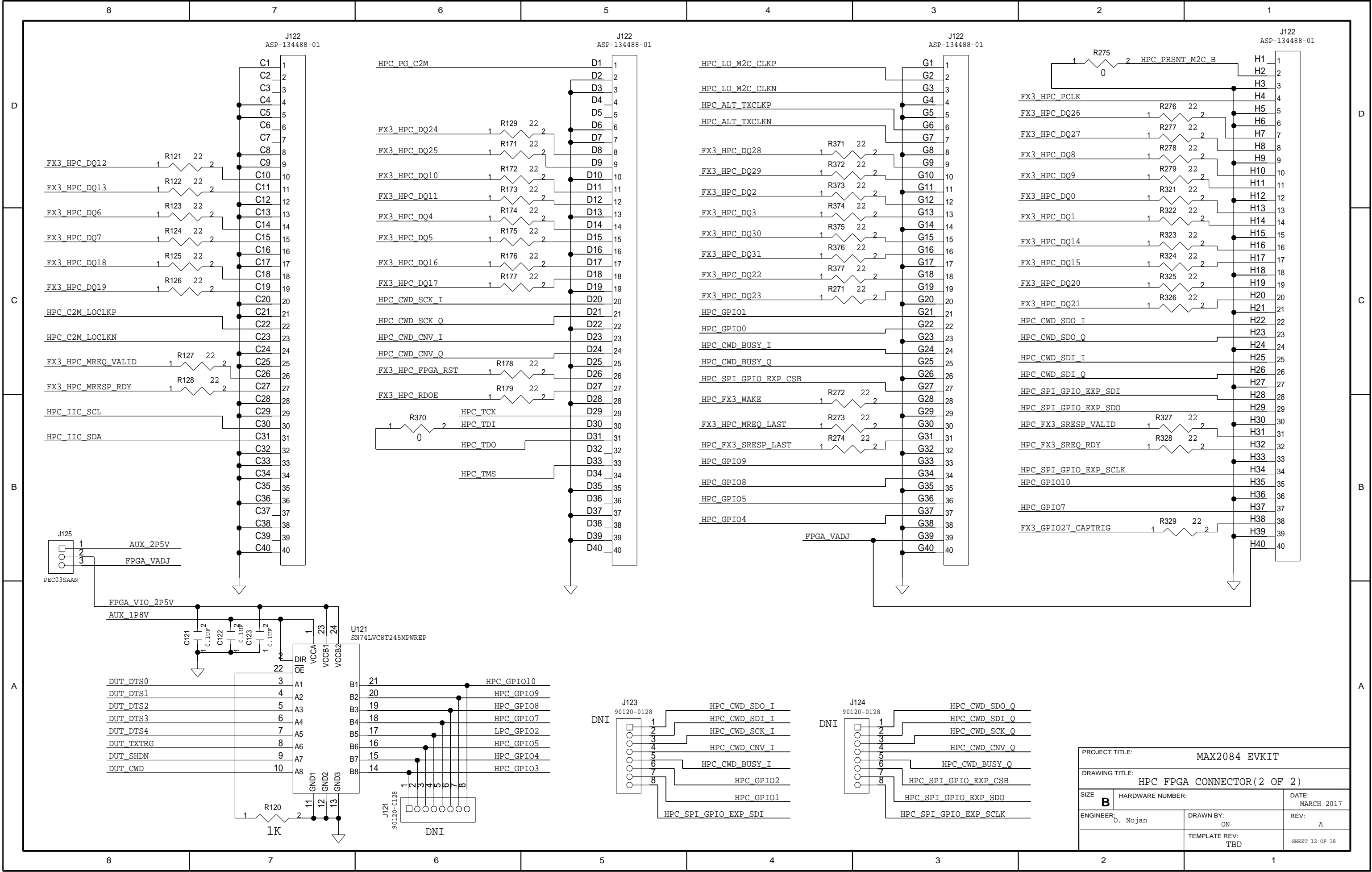
PROJECT TITLE:		
MAX2084 EVKIT		
DRAWING TITLE:		
ON-BOARD RF SOURCES		
SIZE	HARDWARE NUMBER:	DATE:
B		MARCH 2017
ENGINEER:	DRAWN BY:	REV:
O. Nojan	ON	A
	TEMPLATE REV:	
	TBD	
SHEET 9 OF 18		

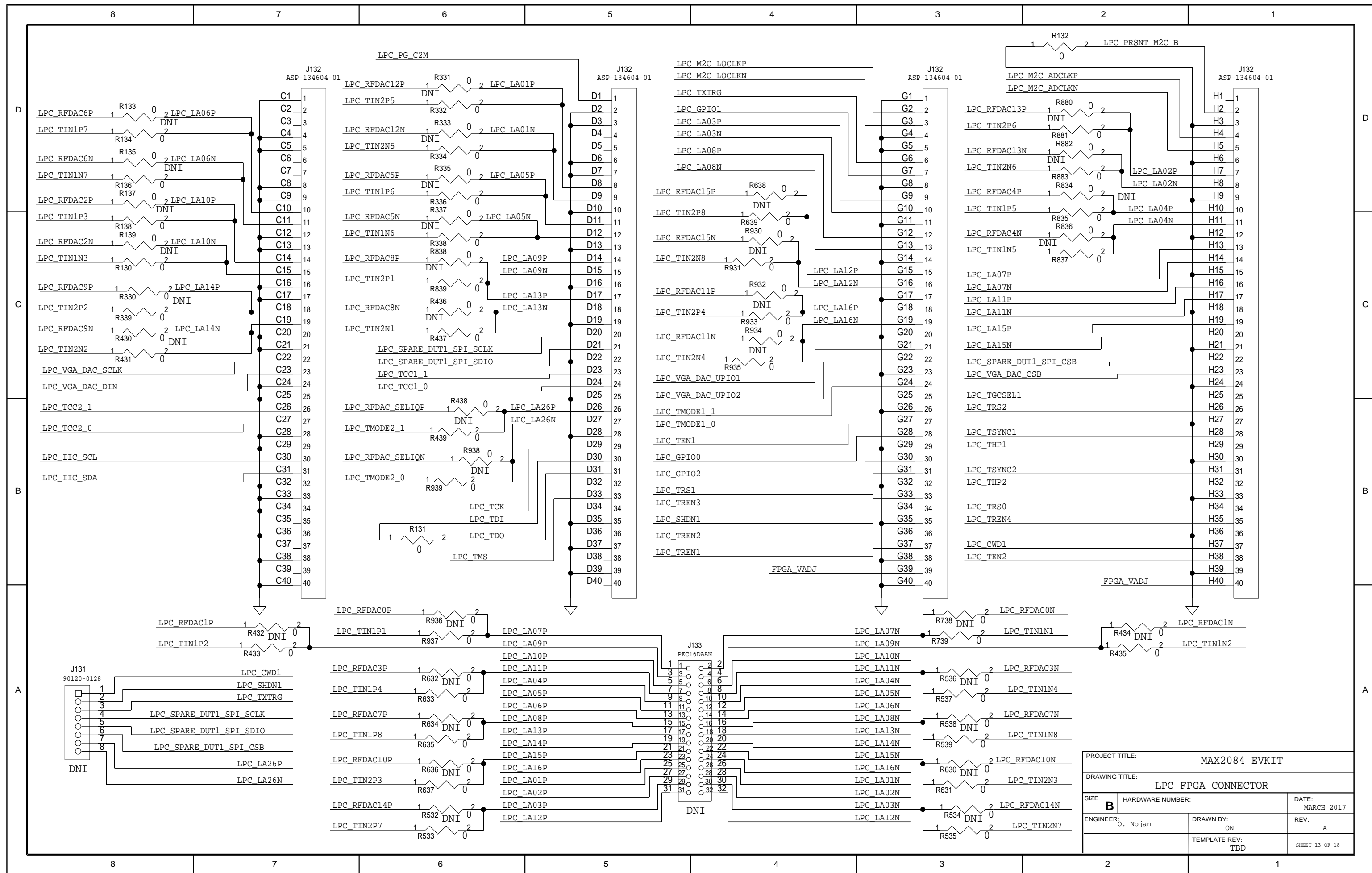


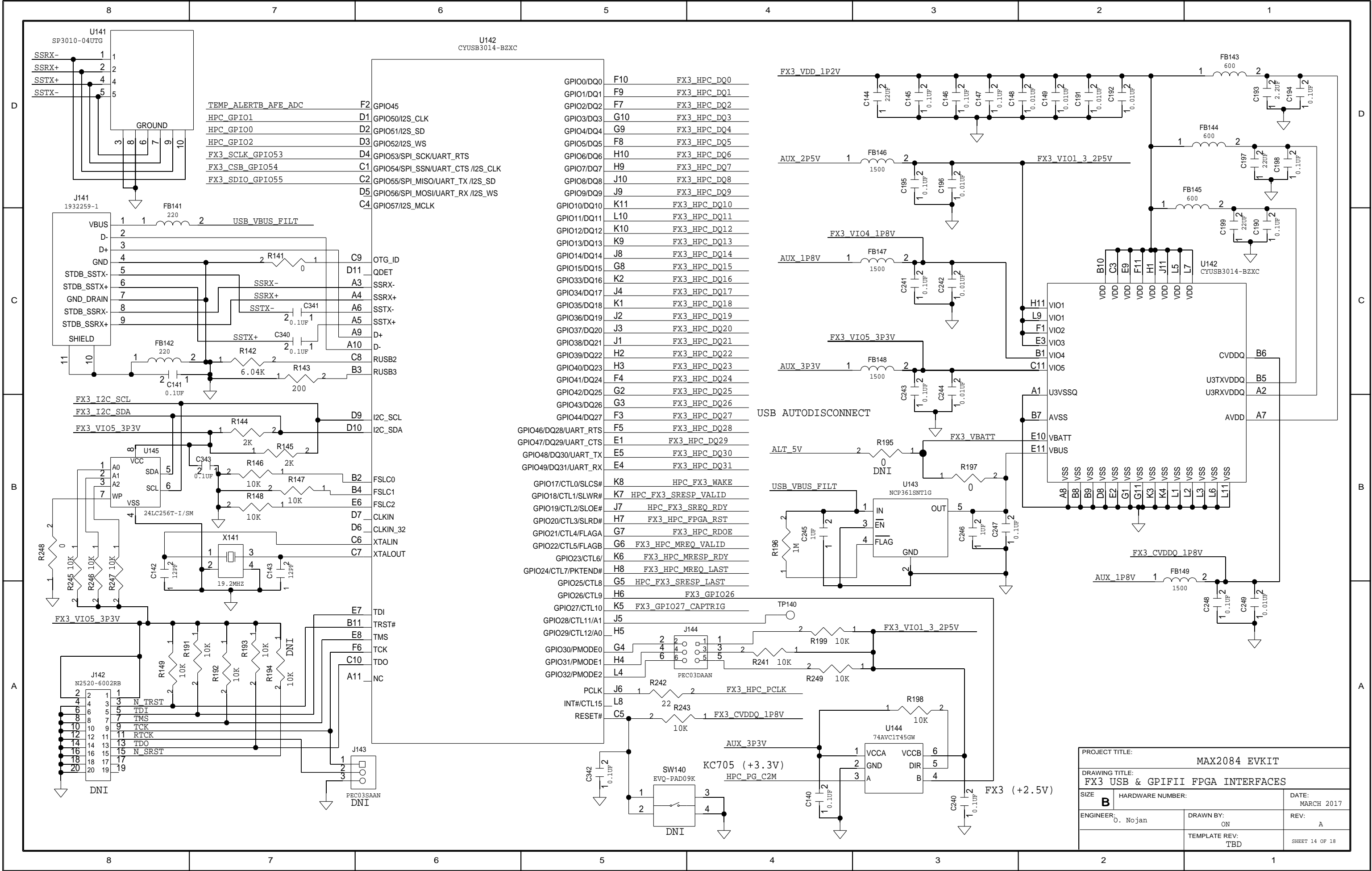
PROJECT TITLE:		MAX2084 EVKIT	
DRAWING TITLE:		TEST MUXES AND TEMP MEASUREMENT	
SIZE	B	HARDWARE NUMBER:	DATE:
			MARCH 2017
ENGINEER:	O. Nojan	DRAWN BY:	REV:
		ON	A
		TEMPLATE REV:	SHEET 10 OF 18
		TBD	

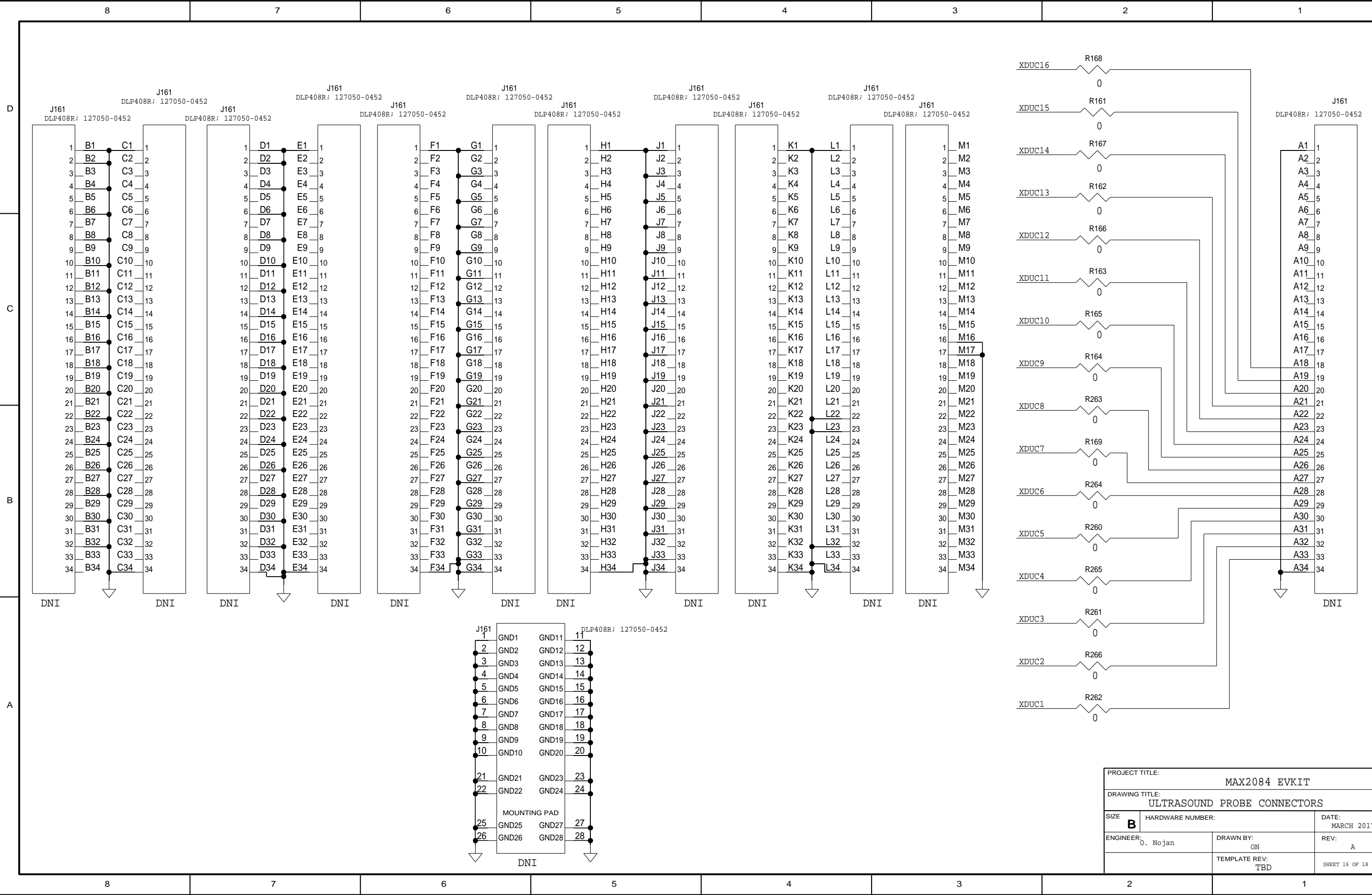


PROJECT TITLE:			MAX2084 EVKIT		
DRAWING TITLE:			HPC FPGA CONNECTOR(1 OF 2)		
SIZE	HARDWARE NUMBER:		DATE:		MARCH 2017
B					
ENGINEER:	O. Nojan		DRAWN BY:	ON	
			TEMPLATE REV:	TBD	
				SHEET 11 OF 18	

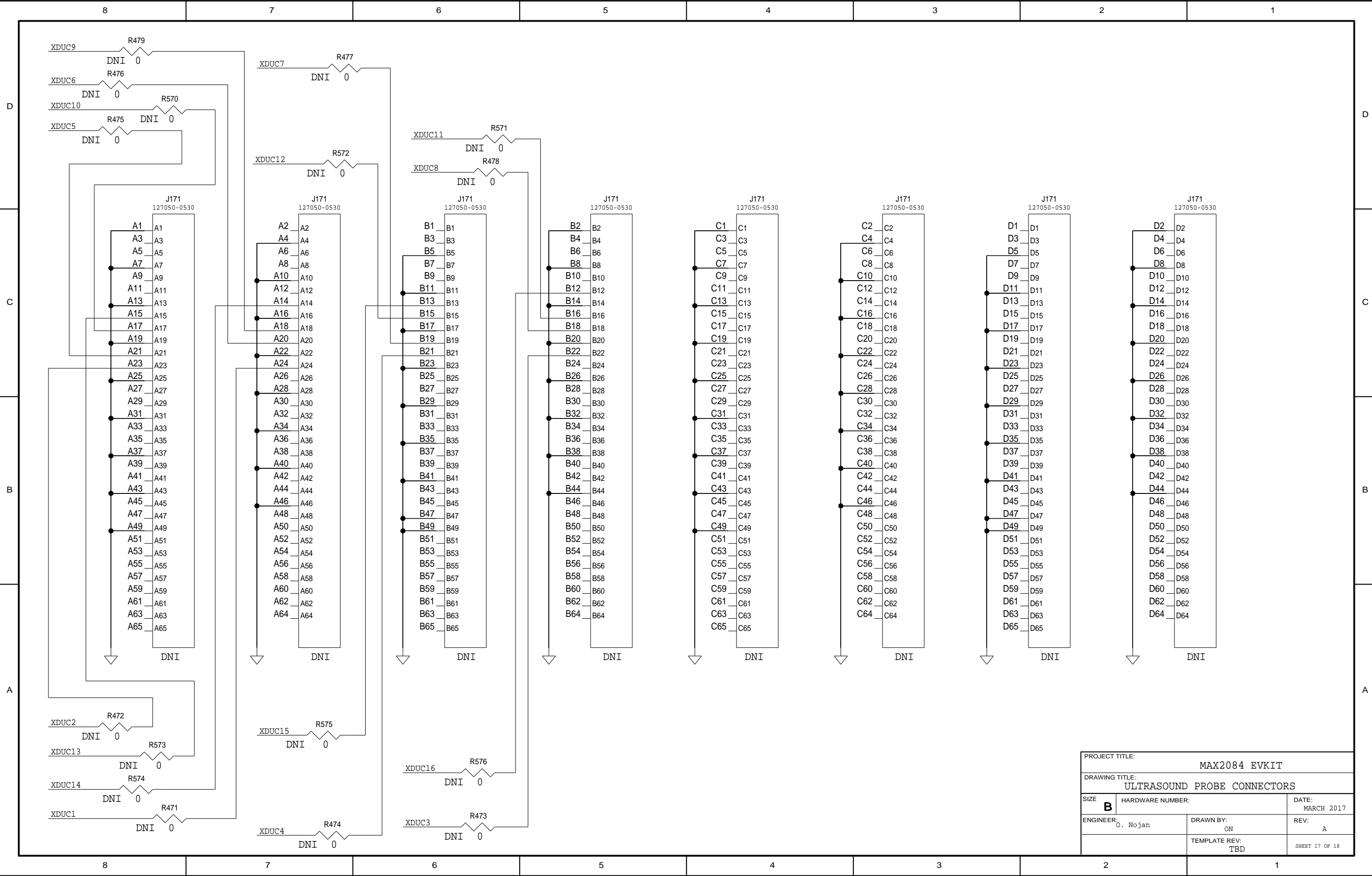








PROJECT TITLE:		MAX2084 EVKIT	
DRAWING TITLE:		ULTRASOUND PROBE CONNECTORS	
SIZE B	HARDWARE NUMBER:		DATE: MARCH 2017
ENGINEER: O. Nojan		DRAWN BY: ON	REV: A
		TEMPLATE REV: TBD	SHEET 16 OF 18



PROJECT TITLE:			MAX2084 EVKIT		
DRAWING TITLE:			ULTRASOUND PROBE CONNECTORS		
SIZE	HARDWARE NUMBER:		DATE:		
B			MARCH 2017		
ENGINEER:		DRAWN BY:		REV:	
O. Nojan		ON		A	
		TEMPLATE REV:		SHEET 17 OF 18	
		TBD			

