



Note: Actual device pin #1 has a chamfered corner terminal as shown.



ASSEMBLY NOTES:

1. BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
2. REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
3. REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.
4. SEE DETAIL A FOR Y1 AND Y2 INSTALLATION.
5. RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).
6. BOARDS TO BE SHIPPED SINGULATED AFTER ASSEMBLY PROCESS. SMOOTHEN EDGES AND FREE FROM BURRS AFTER DEPANELIZATION PROCESS.
7. COMPONENT TO BE INSTALLED USING PICK AND PLACE MACHINE TO ENSURE POSITIONAL TOLERANCE OF +2/ -0 MIL.

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	07JUL21	K. GAGNE
B	CHANGES PER ECR-106612	24FEB22	R. WHITNEY
C	CHANGES PER ECR-110021	01DEC22	R. WHITNEY
D	CHANGES PER ECR-113567	03APR23	R. WHITNEY
E	CHANGES PER ECR-114324	09MAY23	R. WHITNEY
F	CHANGES PER ECR-116013	10AUG23	R. WHITNEY

PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	APPROVAL		DATE		<div><div></div><div>ANALOG DEVICES</div></div>	WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887																				
	TEMPLATE ENGINEER																									
	DECIMALS	FRACTIONS	ANGLES	07JUL21		TITLE ASSEMBLY EVAL - AD2437A1NZ_BRD CUSTOMER EVALUATION Z																				
	.XX --.010	--1/32	-- 2																							
.XXX --.005																										
.XXXX --.0050																										
MATERIAL	TEST ENGINEER																									
	COMPONENT ENGINEER	07JUL21																								
	M. YAN																									
	TEST PROCESS																									
FINISH	HARDWARE RELEASE	07JUL21				SIZE			FSCM NO			DRAWING NUMBER			REV											
	K. JABATAN																									
	DESIGNER	07JUL21	C															24355			01 - 067887			F		
	M. VICEDO																									
PTD ENGINEER	07JUL21	SCALE				1/1			SHEET			1 OF 2														
K. GAGNE																										
CHECKER																										
DO NOT SCALE DWG																										

