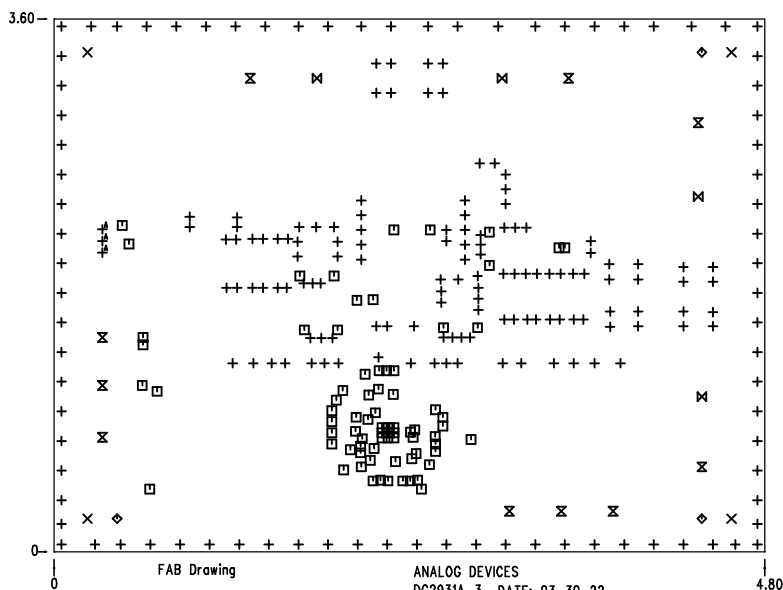
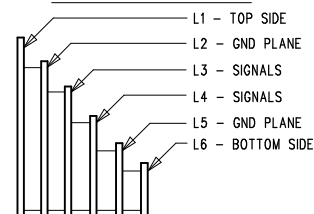


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPR	DATE
-	3	PRODUCTION	GORAN P.	03-30-22



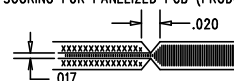
SIZE	QTY	SYM	PLATED	TOL
0.01	76	□	YES	+/-0.003
0.02	209	+	YES	+/-0.003
0.039	3	⊕ <sup>A</sup>	YES	+/-0.003
0.07	3	◇	NO	+/-0.003
0.095	10	⊗	YES	+/-0.003
0.187	4	⊗	NO	+/-0.003
0.195	4	⊗	YES	+/-0.003

## LAYER STRUCTURE



## NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.  
-FINISHED THICKNESS TO BE 0.062+/- .005  
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0/0 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: 0.XXX = ± 0.01 0.XXX = ± 0.005 INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		APPROVALS		ANALOG DEVICES	
		PCB DES.	AN	TITLE: FABRICATION DRAWING	
		APP ENG.	GORAN P.	HIGH EFFICIENCY BUCK-BOOST CONVERTER	
				SIZE	IC NO.
				N/A	LTC7878AUH
					DC2931A
				SCALE = NONE	REV 3
				FILENAME: DC2931A-3.PCB	SHT 1 OF 1