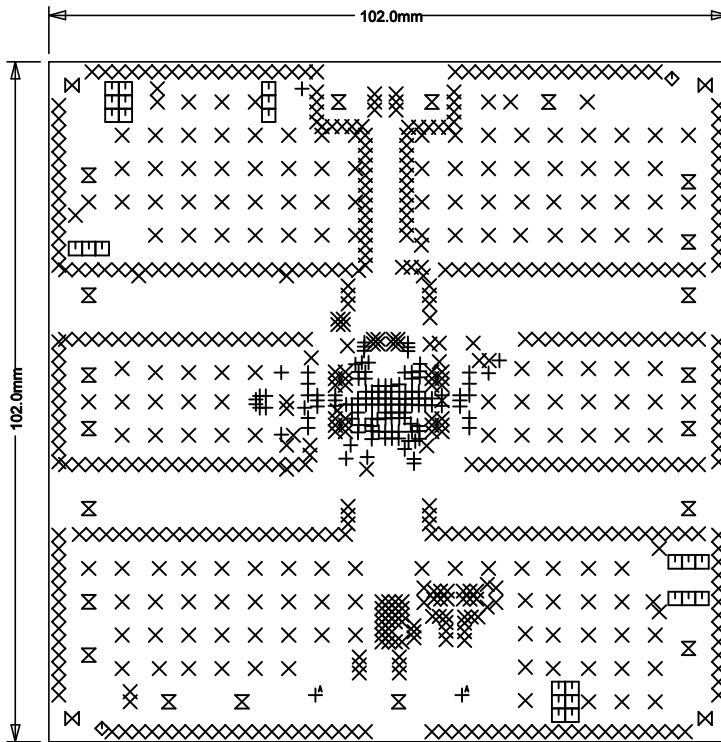


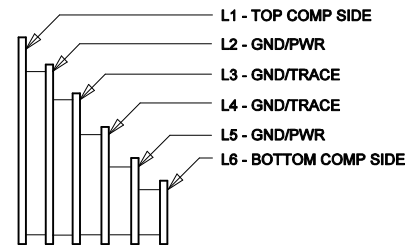
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	NH	4-28-24



L1-COMPONENT SIDE

SIZE	QTY	SYM	PLATED	TOL
0.2	128	+	YES	+/-0.08
0.25	657	X	YES	+/-0.08
0.9	24	□	YES	+/-0.08
1.8	2	◇	NO	+/-0.08
2.41	20	⊗	YES	+/-0.13
4.75	4	⊗	NO	+/-0.13
5.2	2	⊕ ^A	YES	+/-0.13

LAYER CONSTRUCTION



NOTES: UNLESS OTHERWISE SPECIFIED,

- ARTWORK P/N DC2820A-B REV 2.
- FAB PER IPC-A-600
- PCB SHALL MEET RoHS COMPLIANCE.
- MATERIAL:
 - LAMINATE: EPOXY FIBERGLASS, NEMA GRADE FR-4
FLAMMABILITY RATING: 94 V-2 MINIMUM .
 - THICKNESS: 1.6 +/- 0.1 mm
 - CLAD: 2 OZ. ON ALL LAYERS.
- FINISH:
 - COATING: SOLDERMASK OVER BARE COPPER,
COLOR, GREEN LPI.
 - PLATING: GOLD IMMERSION BOTH SIDES.
 - SILKSCREEN: BOTH COMPONENT SIDES WITH
WHITE NON-CONDUCTIVE INK.
- TOLERANCES:
 - WARPAGE: (0.075mm) MAX. ALONG THE LONGEST
DIAG. AS PER IPC-TM650 METHOD 2.4.22.
 - ETCHING: +0.03mm, -0.3mm OF MASTER PATTERN.
 - REGISTRATION: 0.075mm MAX.
 - MASK THICKNESS: 0.025mm MIN., 0.075mm MAX.
 - HOLE PLATING: 22um +/- 3um.
- SIZE:
 - CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
- HOLE DIAMETERS ARE POST-PLATED SIZES.
- DEBURR AND BREAK ALL SHARP EDGES.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- PLUG ALL VIA'S WITH NON-CONDUCTIVE EPOXY AND
PLATE OVER. PLANARIZE FINAL SURFACE.

UNLESS OTHERWISE SPECIFIED		APPROVALS		ANALOG DEVICES	
DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		PCB DES.	HZ	TITLE: FABRICATION DRAWING 40VIN QUAD 3A SILENT SWITCHER μMODULE REGULATOR WITH EMI SHIELD	
		APP ENG.	NH		
				SIZE N/A	IC NO. LTM8060FEV DC2820A-B
		SCALE = NONE		FILENAME:DC2820A-B.PCB	REV 2
				SHT 1 OF 1	

