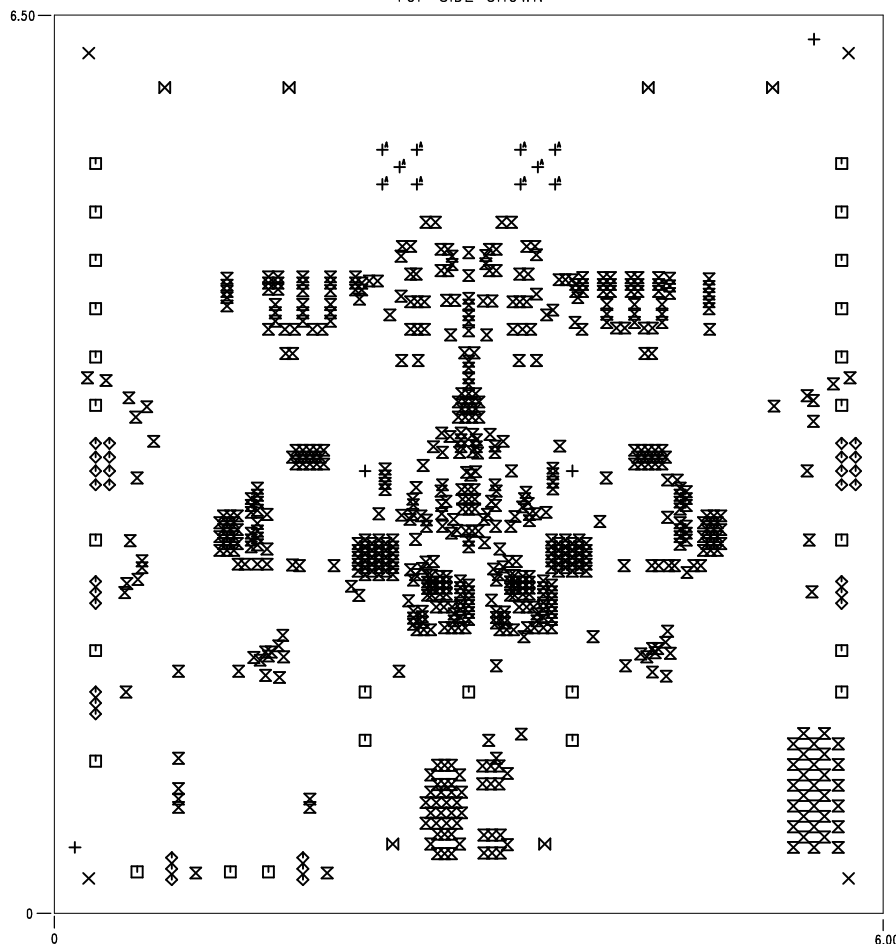
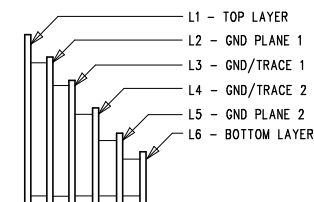


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	MIKE S.	6-04-13

TOP SIDE SHOWN

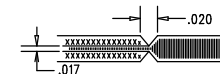


LAYER STRUCTURE



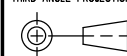
NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062 +/- .005
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
O/O ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



SIZE	QTY	SYM	PLATED	TOL
0.19	6	⊗	YES	+/-0.003
0.187	4	⊗	YES	+/-0.003
0.095	26	□	YES	+/-0.003
0.07	4	+	NO	+/-0.003
0.055	10	⊕	YES	+/-0.003
0.035	31	◇	YES	+/-0.003
0.01	679	⊗	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
XX = ± 0.01
XXX = ± 0.005
INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION



APPROVALS

PCB DES. LT

APP ENG. MIKE S.

SCALE = NONE

LINEAR TECHNOLOGY
1830 MCCARTHY BLVD
MILPITAS, CA 95035
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www.linear.com
LTC CONFIDENTIAL-
FOR CUSTOMER USE ONLY

TITLE: FABRICATION DRAWING
HIGH EFFICIENCY DUAL OUTPUT STEP-DOWN
CONVERTER WITH VERY LOW DCR INDUCTOR

SIZE IC NO. LTC3774EUH
N/A DEMO CIRCUIT 2002A
REV 2

FILENAME: DC2002A-2.PCB SHT 1 OF 1