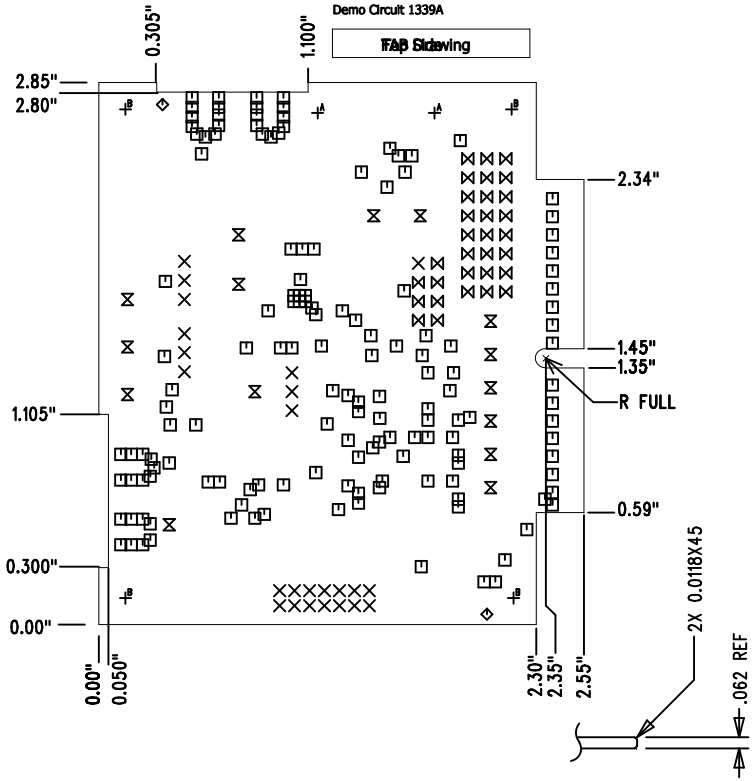
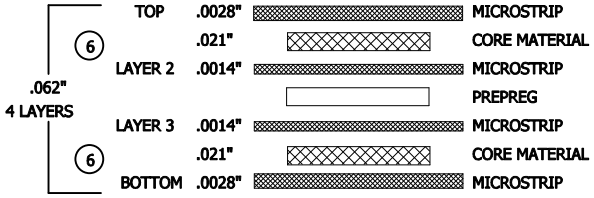
LINEAR TECHNOLOGY CORPORATION
Demo Circuit 1339A
FAB Drawing



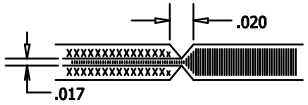
SIZE	QTY	SYM	PLATED	TOL
35	24	X	YES	+/-3Mil
12	148	□	YES	+/-3Mil
70	2	◇	NO	+/-3Mil
63	15	⧖	YES	+/-3Mil
40	31	⋈	YES	+/-3Mil
94	2	+A	YES	+/-3Mil
185	4	+B	NO	+/-3Mil

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600
2. MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4
   FINISHED THICKNESS TO BE 0.062" +/- 0.006"
   TOTAL OF 4 LAYERS WITH 2 OZ. COPPER ON THE EXTERNAL LAYERS AND 1 OZ. COPPER ON THE INTERNAL LAYERS.
   FLAMMABILITY RATING: 94 V-O MINIMUM .
3. FINISH: SMOBC USING LPI BOTH SIDES, COLOR GREEN.
   GOLD IMMERSION.
   FOR SILKSCREEN: USE WHITE NON-CONDUCTIVE INK.
4. DRILLLING: DRILL HOLES PER SCHEDULE. ALL PLATED THROUGH HOLES WITH COPPER, 0.001" MIN. / 0.0015" MAX. COPPER PLATE ALL HOLE SIZES ARE SPECIFIED AFTER PLATING. HOLE LOCATION TOLERANCES ARE +/-0.01" IN RELATION TO CENTER
5. SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT LTC.
6. FINGER AREA: GOLD PLATE TO MIN. OF 0.00003 INCHES. PER MIL-G-45204, TYP II, CL1, GRADE C, OVER 0.0002 INCHES LOW STRESS NICKEL PER QQ-N-290, CL2.
7. DO NOT ALTER ARTWORK TO ADD LOGO OR DATE CODE BUT YOU MAY MODIFY PAD SIZE TO MEET END FINISH.
8. PCB's SHOULD BE RoHS COMPLIANT.
9. SCORING FOR PANELIZED PCB:



APPROVALS	INIT	DATE
DRAWN		
CHECK		
DESIGN	RudyB	
ENGR	Guy Hoover	

SCALE = NONE

LINEAR TECHNOLOGY
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TITLE: LTC2302CDD
12-Bit, Low Noise ADC

FD | Demo Circuit 1339A | REV. 0

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