

AD9371/AD9375 Software Release Notes

Date: November 7th, 2017

The links and release notes for the updated AD9371 Transceiver Evaluation Software version 2.0.68 and associated API and ARM code are below. **This code update applies to Rev1.0 and Rev 1.1 silicon only and is not backwards compatible with older versions.** It may be necessary to copy and paste the links into Internet Explorer for proper operation (links are only valid for Internet Explorer).

V2.0.68 GUI – notes reflect changes since version 2.0.67

1. Updated API/DLL version.
2. Profile selection table updated.

V3566.0 API

1. API changes in this release are limited to updated comments within the API files (no code changes).
 - a. Corrected a comment in the Mykonos_setRfDcOffsetCnt function.
 - b. Added updates to comments to clarify CLGC error codes.

Release v5.2.2 ARM

ARM Release Notes:

1. Added a TxRMS check for CLGC to determine if the TX input power is $< -39\text{dBFS}$, if true CLGC reports a warning and does not update.
2. Corrected a sequencing issue where CLGC tuning preset mode operation was reporting a low ORX level rather than first updating to new attenuation level based on an updated setting.
3. Resolved an issue where LO Leakage tracking was incorrectly reporting error 6 and error 9 and also resolved a similar issue where TX QEC was incorrectly reporting error 6. These incorrect errors occurred only in very specific TDD timing scenarios.

ARM Firmware Verification Notes:

1. RX QEC can degrade if large CW signal is placed directly on DC
2. RX QEC performance can degrade if channel remains in a continuous overload condition.
3. The sniffer channel QEC operation is not supported above 5GHz for this release.
4. The CLGC relative threshold feature can intermittently fail to report an issue if the change is only slightly above the threshold – this is due to transition occurring within data capture window.
5. When DPD is enabled in TDD mode in certain uplink/downlink configurations TX QEC tracking and VSWR are not updating (AD9373 Only). Consult ADI applications team for workaround options.
6. External LO leakage init can have reduced performance when run after running LO leakage tracking.
7. VSWR can have reduced accuracy at low signal input power levels.
8. CLGC can report error 7 intermittently under certain conditions – these do not impact operation and can be ignored.