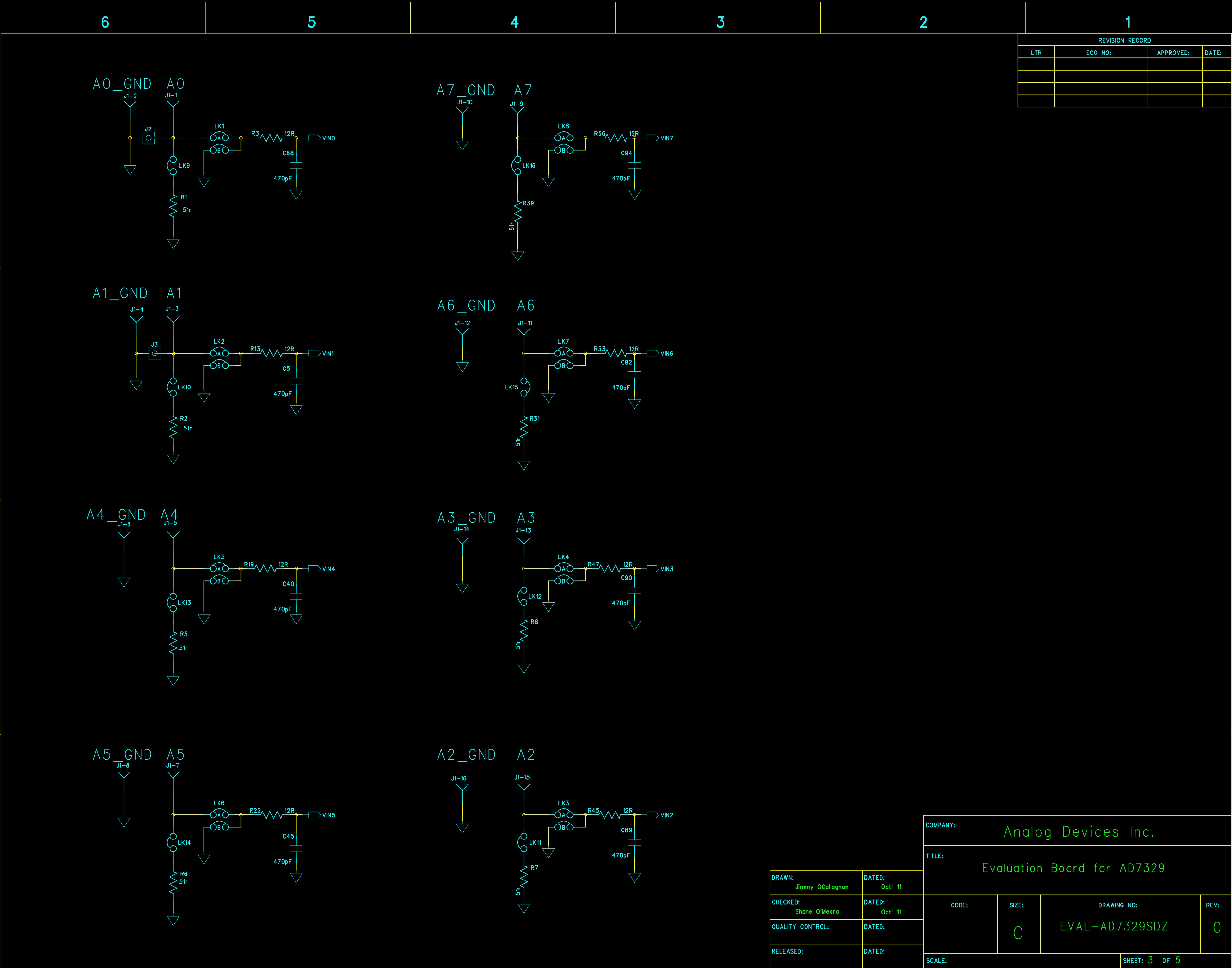


REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

COMPANY: Analog Devices Inc.			
TITLE: Evaluation Board for AD7329			
CHECKED: Shane O'Meara	DATED: Oct' 11	CODE:	SIZE:
QUALITY CONTROL: <QC By>	DATED: <QC Date>	DRAWING NO: EVAL-AD7329SDZ	
RELEASED: <Released By>	DATED: <Release Date>	REV: 0	
SCALE: <Scale>			SHEET: 1 OF 5

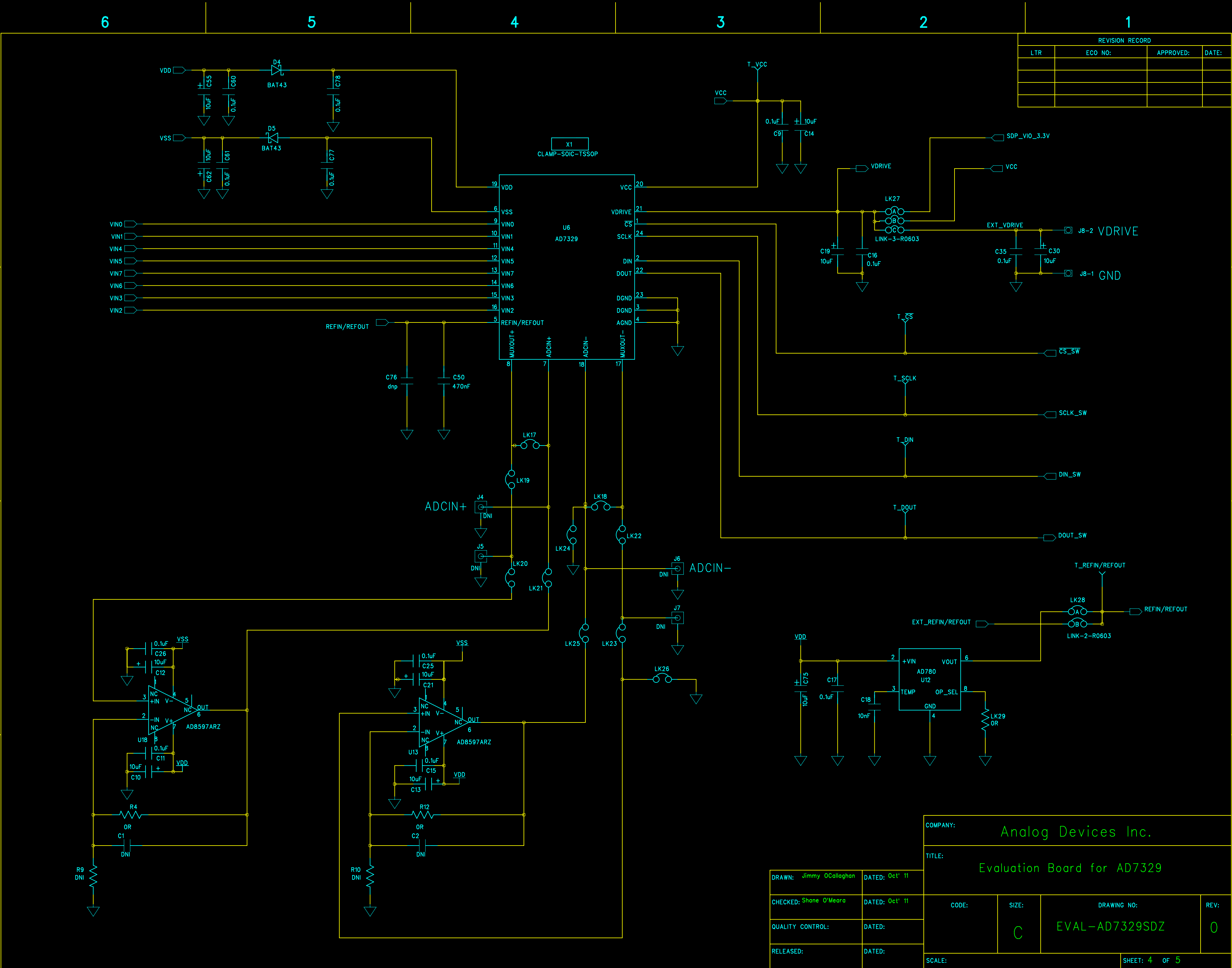




REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

COMPANY: Analog Devices Inc.			
TITLE: Evaluation Board for AD7329			
CODE:	SIZE: C	DRAWING NO: EVAL-AD7329SDZ	REV: 0
SCALE:		SHEET: 3 OF 5	

DRAWN: Jimmy O'Callaghan	DATED: Oct' 11
CHECKED: Shane O'Meara	DATED: Oct' 11
QUALITY CONTROL:	DATED:
RELEASED:	DATED:



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

DRAWN: Jimmy O'Callaghan	DATED: Oct' 11
CHECKED: Shane O'Meara	DATED: Oct' 11
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

COMPANY: Analog Devices Inc.			
TITLE: Evaluation Board for AD7329			
CODE:	SIZE: C	DRAWING NO: EVAL-AD7329SDZ	REV: 0
SCALE:		SHEET: 4 OF 5	

6

5

4

3

2

1

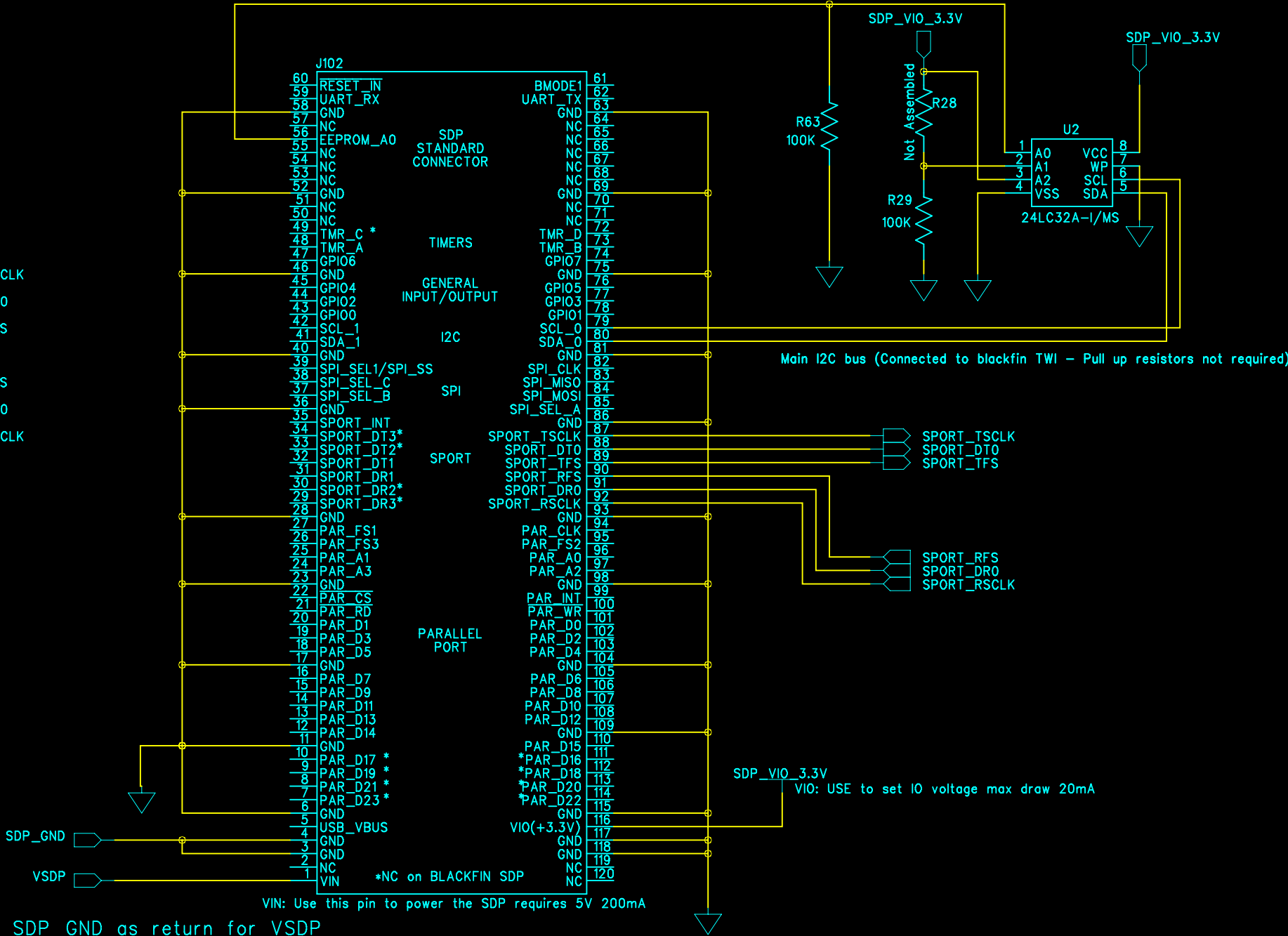
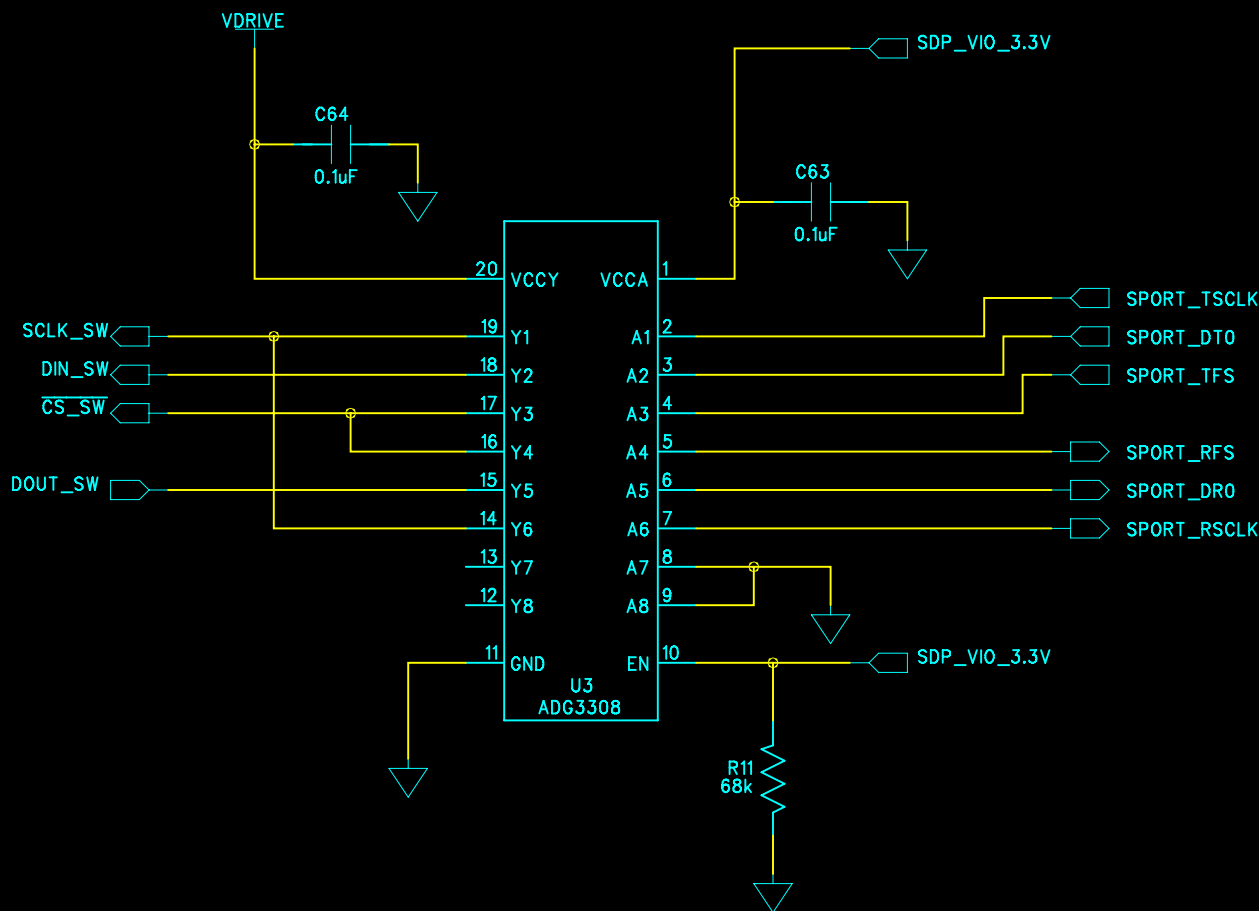
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

I2C bus 1 is common across both connectors on SDP – Pull up resistors required

BMODE1: Pull up with a 10K resistor to set SDP to boot from a SPI FLASH on the daughter board

Board ID EEPROM (24LC32) must be on I2C bus 0,

Board ID EEPROM (24LC32) must be on I2C bus 0,



Use SDP\_GND as return for VSDP

VIN: Use this pin to power the SDP requires 5V 200mA

SDP\_VIO\_3.3V  
VIO: USE to set IO voltage max draw 20mA

D

C

B

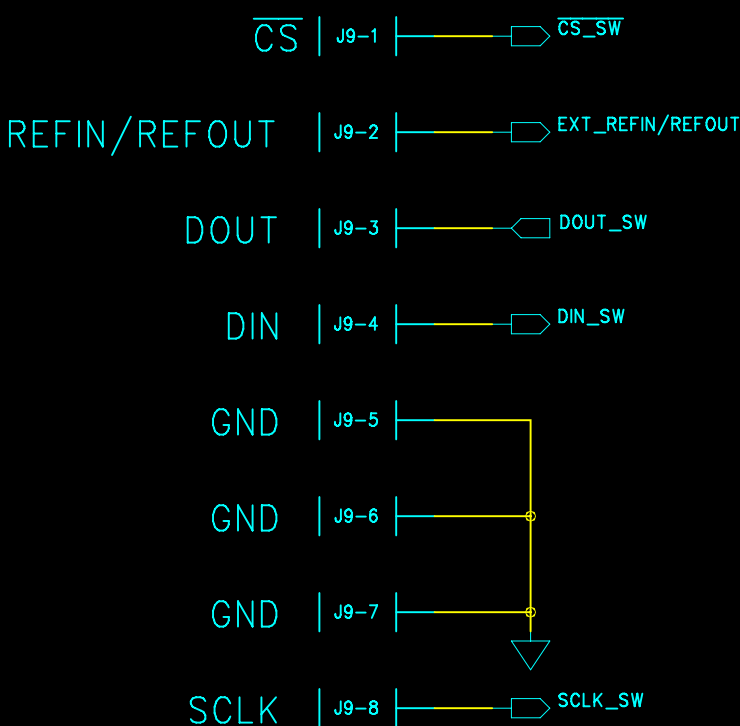
A

D

C

B

A



DRAWN: Jimmy O'Callaghan	DATED: Oct' 11
CHECKED: Shane O'Meara	DATED: Oct' 11
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

COMPANY: Analog Devices Inc.			
TITLE: Evaluation Board for AD7329			
CODE:	SIZE: C	DRAWING NO: EVAL-AD7329SDZ	REV: 0
SCALE: <Scale>			SHEET: 5 OF 5