

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

COMPANY: Analog Devices Inc.			
TITLE: EVAL-ADuC7060 Schematic			
CODE:	SIZE:	DRAWING NO:	REV:
EVAL-ADuC7060QS			A1
SCALE:		SHEET: 1 OF 1	

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D

C

B

A

Include footprint for MOSFET, 6 x Res, 2 x CAP

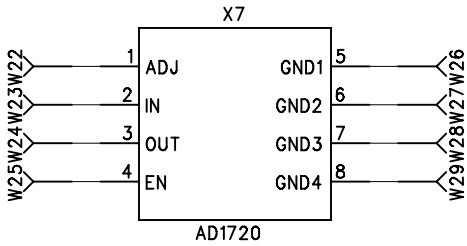
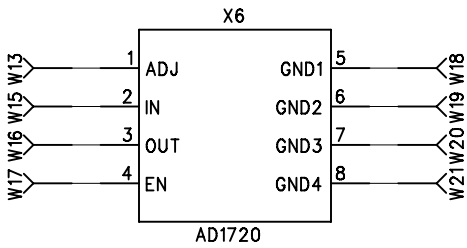
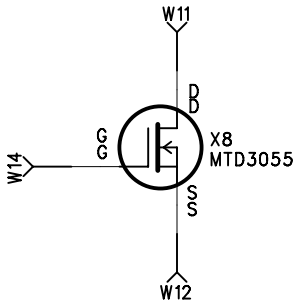
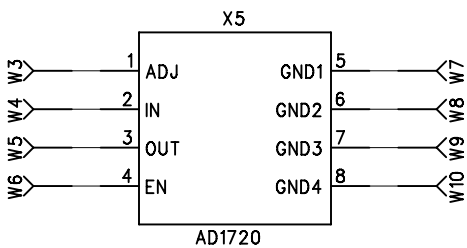
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SCALE:		SHEET: OF	

DRAWN:	DATED:
CHECKED:	DATED:
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

6

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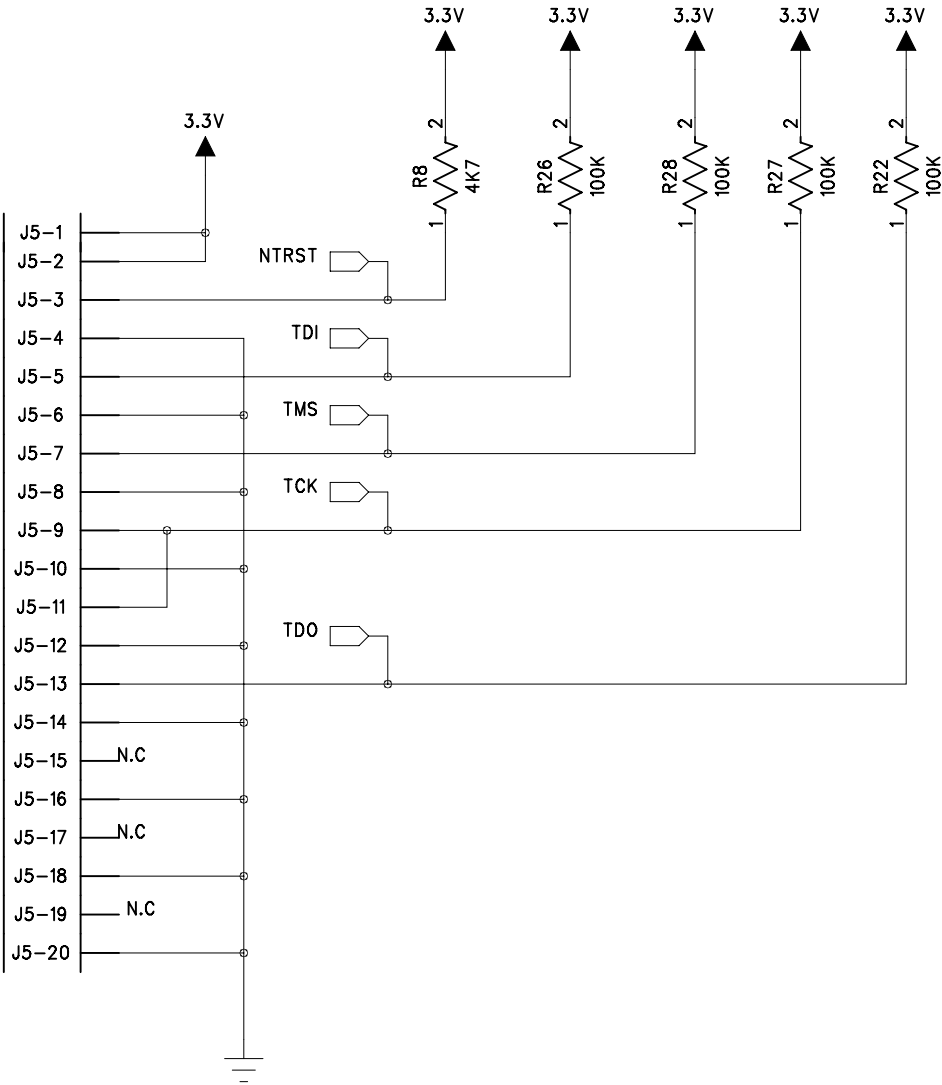
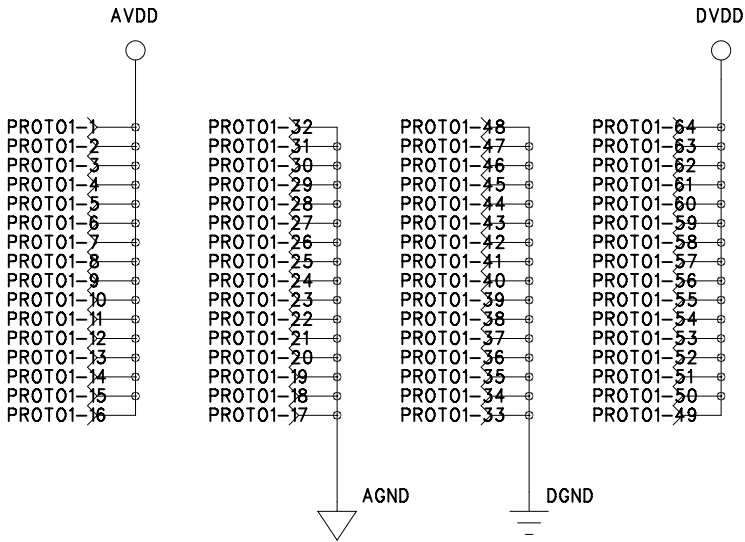
3

2

1

REVISION RECORD

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CHECKED:	DATED:	CODE:	SIZE:
QUALITY CONTROL:	DATED:	DRAWING NO:	
RELEASED:	DATED:	REV:	
SCALE:		SHEET: OF	